IAR Embedded Workbench® for STM8

IAR Embedded Workbench is a set of highly sophisticated and easy-to-use development tools for embedded applications. It integrates the IAR C Compiler™, assembler, linker, librarian, text editor, project manager, and C-SPY® Debugger in an integrated development environment (IDE). With its built-in chip-specific code optimizer, IAR Embedded Workbench generates very efficient and reliable FLASH/PROMable code for STM8 devices. In addition to this solid technology, IAR Systems also provides professional worldwide technical support.

MODULAR AND EXTENSIBLE IDE

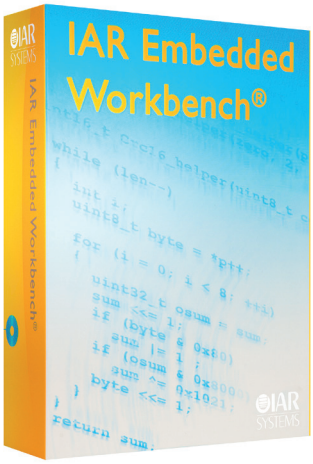
- A seamlessly integrated environment for building and debugging embedded applications
- Powerful project management allowing multiple projects in one workspace
- Build integration with IAR visualSTATE
- Hierarchical project representation
- Dockable and floating windows management
- Smart source browser
- Feature-rich editor with code templates and multi-byte support
- Tool options configurable on global, group of source files, or individual source files level
- Flexible project building via batch build, pre/post-build or custom build with access to external tools in the build process
- Integration with source code control systems
- Extensive device support with ready-made header files, device description files and linker command files
- Ready-made code and project examples

HIGHLY OPTIMIZING C COMPILER

- ISO/ANSI C compiler
- Support for the C99 standard
- Full support for the STM8A, STM8L and STM8S families
- Up-to-date device support list available at www.iar.com/ewstm8
- Automatic checking of MISRA C rules
- Language extensions for embedded applications with target-specific support:
  - Extended keywords for data/functions defining and declaring with memory/type attributers
  - Pragma directives for controlling the compiler's behavior, such as how it allocates memory
  - Intrinsic functions for direct access in C source to lowlevel processor operations
- Efficient interrupt handling directly in C
- 32-bit IEEE-compatible floating-point arithmetic
- Mixed C and assembler listings
- Support for inline assembler
- Highly optimized reentrant code models making the project portable between different targets
- Multiple levels of optimizations on code size and execution speed allowing different transformations enabled, such as function inlining, loop unrolling etc.
- Advanced global and target-specific optimizer generating the most compact and stable code
- Multi-file compilation support

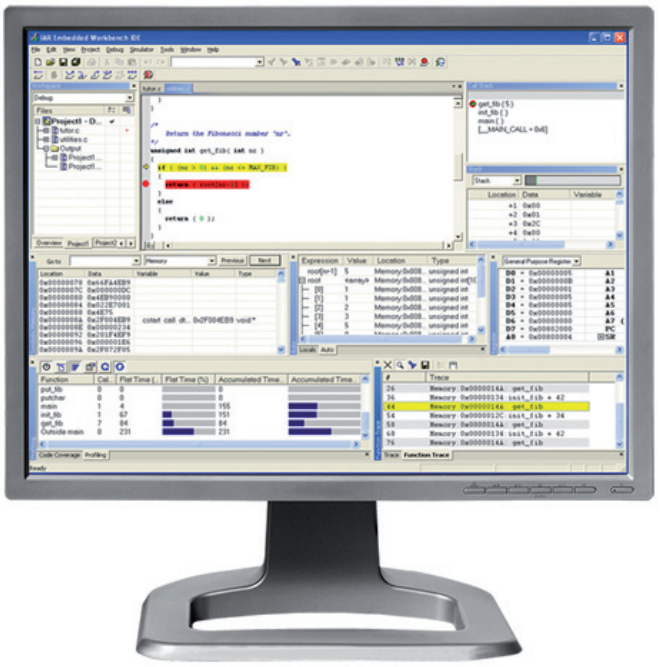
STATE-OF-THE-ART C-SPY® DEBUGGER

- Complex code and data breakpoints
- Very fine granularity execution control (function call-level stepping)
- Stack window to monitor the memory consumption and integrity of the stack
- Complete support for stack unwinding even at high optimization levels
- Profiling and code coverage performance analysis tools
- Trace utility with expressions to examine execution history
- Versatile monitoring of registers, structures, call chain, locals, global variables and peripheral registers
- Symbolic memory window and static watch window



IAR SYSTEMS

- I/O and interrupt simulation
- True editing-while-debugging
- Convenient file/function/variable accessing in different windows with drag and drop model
- RTOS-aware debugging with built-in plugins for OSEK Run Time Interface (ORTI)
- Hardware debugging support for the ST-LINK debugger probe and STice emulator
- C-SPY Debugger simulator:
  - Instruction-level simulation
  - Memory configuration and validation
  - Interrupt simulation
  - Peripheral simulation, using the debugger macro system in conjunction with immediate breakpoints



IAR ASSEMBLER

- A powerful relocating macro assembler with a versatile set of directives and operators
- Built-in C language preprocessor, accepting all C macro definitions

IAR ILINK LINKER

- Complete linking, relocation and format generation to produce FLASH/PROMable code
- Flexible commands allowing detailed control of code and data placment
- Optimized linking removing unused code and data
- Direct linking of raw binary images, for instance multimedia files
- Optional code checksum generation for runtime checking
- Comprehensive cross-reference and dependency memory maps

IAR LIBRARY AND LIBRARY TOOLS

- All required ISO/ANSI C libraries included
- All low-level routines such as writechar and readchar provided in full source code
- Lightweight runtime library, user-configurable to match the needs of the application; full source included
- Library tools for creating and maintaining library projects, libraries and library modules
- Listings of entry points and symbolic information

COMPREHENSIVE DOCUMENTATION

- PDF user guides with detailed information
- Efficient coding hints for embedded application
- Extensive step-by-step tutorials
- Context sensitive help and hypertext versions of the user documentation available online

FREE EVALUATION SOFTWARE

Free evaluation softwares—30-day evaluation version is available at http://www.iar.com/ewstm8.

For the latest product news, up-to-date device support list, hardware debugging support and related tools, please visit http://www.iar.com/ewstm8

IAR visualSTATE®

IAR visualSTATE is a suite of graphical design automation tools for embedded systems.

- Design an embedded application by drawing objects, events, actions etc in a flowchart-like manner
- Perform extensive tests before committing to hardware: validation of the application behavior, regression testing, verification of the run-time model and simulation on-chip
- Automatically generate micro-tight C/C++ code that is 100% consistent with your design as well as complete design documentation

Together with IAR Embedded Workbench, IAR visualSTATE forms a complete set of development tools for the STM8 microcontrollers, supporting you through the entire development process.
From Idea to Target®

www.iar.com

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