

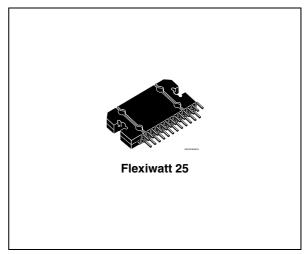
4 x 45 W MOSFET quad bridge power amplifier

Features

- Multipower BCD technology
- High output power capability:
 - $-4 \times 45 \text{ W/4 } \Omega \text{ Max.}$
 - 4 x 28 W/4 Ω @ 14.4 V, 1 kHz, 10 %
 - 4 x 72 W/2 Ω Max.
- MOSFET output power stage
- \blacksquare Excellent 2 Ω driving capability
- Hi-Fi class distortion
- Low output noise
- Standby function
- Mute function
- Automute at min. supply voltage detection
- Low external component count:
 - Internally fixed gain (26 dB)
 - No external compensation
 - No bootstrap capacitors

Protections

- Output short circuit to GND, to V_S, across the load
- Very inductive loads
- Overrating chip temperature with soft thermal limiter
- Load dump voltage
- Fortuitous open GND



- Reversed battery
- ESD

Description

The TDA7851L is a breakthrough MOSFET technology class AB audio power amplifier in Flexiwatt25 package designed for high power car radio

The fully complementary P-Channel/N-Channel output structure allows a rail-to-rail output voltage swing which, combined with high output current and minimized saturation losses sets new power references in the car-radio field, with unparalleled distortion performances.

Table 1. Device summary

Order code	Package	Packing
TDA7851L	Flexiwatt 25	Tube

Contents TDA7851L

Contents

1	Bloc	k diagram and application circuit
	1.1	Block diagram
	1.2	Application circuit
2	Pin o	description6
	2.1	Pin connection 6
	2.2	Thermal data 6
3	Elec	trical specifications7
	3.1	Absolute maximum ratings
	3.2	Electrical characteristics
	3.3	Electrical characteristic curves
4	Арр	lication hints 12
	4.1	SVR 12
	4.2	Input stage 12
	4.3	Standby and muting
	4.4	Heatsink definition
5	Pack	rage information
6	Revi	sion history14

TDA7851L List of tables

List of tables

	Device summary	
Table 2.	Thermal data	6
Table 3.	Absolute maximum ratings	7
Table 4.	Electrical characteristics	7
Table 5.	Document revision history	4

List of figures TDA7851L

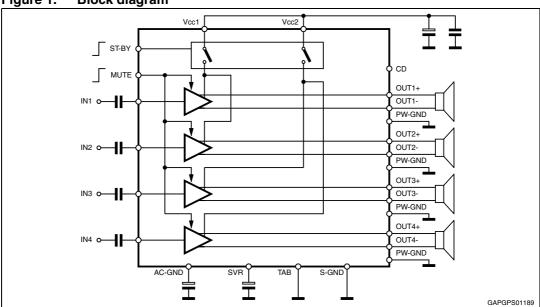
List of figures

Figure 1.	Block diagram	5
Figure 2.	Application circuit	5
Figure 3.	Pin connection (top view)	
Figure 4.	Quiescent current vs. supply voltage	
Figure 5.	Output power vs. supply voltage ($R_1 = 4 \Omega$)	
Figure 6.	Output power vs. supply voltage $(R_1 = 2 \Omega)$	9
Figure 7.	Distortion vs. output power ($R_1 = 4 \Omega$)	9
Figure 8.	Distortion vs. output power ($R_1 = 2 \Omega$)	9
Figure 9.	Distortion vs. frequency ($R_L = 4 \Omega$)	9
Figure 10.	Distortion vs. frequency ($R_L = 2 \Omega$)	. 10
Figure 11.	Crosstalk vs. frequency	. 10
Figure 12.	Supply voltage rejection vs. frequency	. 10
Figure 13.	Output attenuation vs. supply voltage	. 10
Figure 14.	Power dissipation and efficiency vs. output power ($R_L = 4 \Omega$, SINE)	. 10
Figure 15.	Power dissipation and efficiency vs. output power ($R_L = 2 \Omega$, SINE)	. 10
Figure 16.	Power dissipation vs. output power ($R_L = 4 \Omega$, audio program simulation)	. 11
Figure 17.	Power dissipation vs. output power ($R_L = 2 \Omega$, audio program simulation)	. 11
Figure 18.	ITU R-ARM frequency response, weighting filter for transient pop	. 11
Figure 10	Fleviwatt25 mechanical data and nackage dimensions	13

1 Block diagram and application circuit

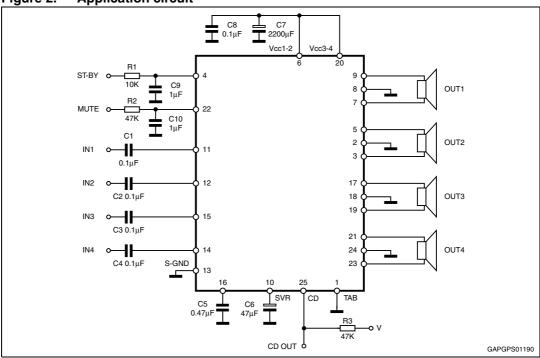
1.1 Block diagram

Figure 1. Block diagram



1.2 Application circuit

Figure 2. Application circuit

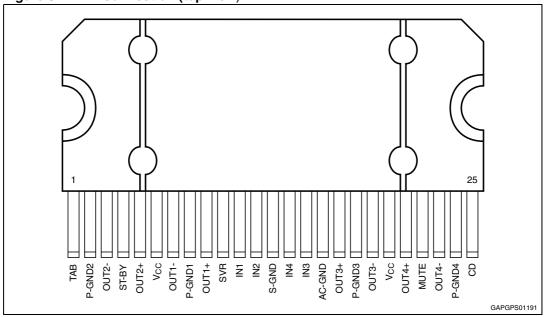


Pin description TDA7851L

2 Pin description

2.1 Pin connection

Figure 3. Pin connection (top view)



2.2 Thermal data

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{th j-case}	Thermal resistance junction-to-case Max	1	°C/W

3 Electrical specifications

3.1 Absolute maximum ratings

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _S	Operating supply voltage	18	V
V _{S (DC)}	DC supply voltage	28	V
V _{S (pk)}	Peak supply voltage (for t = 50 ms)	50	V
I _O	Output peak current Non repetitive (t = 100 µs) Repetitive (duty cycle 10 % at f = 10 Hz)	10 9	A A
P _{tot}	Power dissipation T _{case} = 70 °C	85	W
T _j	Junction temperature	150	°C
T _{amb}	Operating temperature range	-40 to 105	°C
T _{stg}	Storage temperature	-55 to 150	°C

3.2 Electrical characteristics

Refer to the test and application diagram, V_s = 14.4 V; R_L = 4 Ω ; R_g = 600 Ω ; f = 1 kHz; T_{amb} = 25 °C; unless otherwise specified.

Table 4. Electrical characteristics

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
V _S	Supply voltage range	-	8	-	18	V
I _{q1}	Quiescent current	$R_L = \infty$	100	150	300	mA
V _{OS}	Output offset voltage	Play mode / Mute mode	-60	-	+60	mV
dV	During mute on/off output offset voltage	ITU R-ARM weighted	-10	-	+10	mV
dV _{OS}	During standby on/off output offset voltage	see Figure 18	-10	-	+10	mV
G _v	Voltage gain	-	25	26	27	dB
dG _v	Channel gain unbalance	-			±1	dB
В	Output power	V _S = 14.4 V; THD = 10 % V _S = 14.4 V; THD = 1 %	25 -	28 22	-	W W
P _o	Output power	V_S = 14.4 V; THD = 10 %, 2 Ω V_S = 14.4 V; THD = 1 %, 2 Ω	-	48 38	-	W W
P _{o max.}	Max. output power ⁽¹⁾	$V_S = 14.4 \text{ V}; R_L = 4 \Omega$ $V_S = 14.4 \text{ V}; R_L = 2 \Omega$	-	45 75	-	W W
THD	Distortion	P _o = 4 W	-	0.01	0.05	%

Table 4. Electrical characteristics (continued)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
e _{No}	Output noise	"A" Weighted	_	35	-	μV
- 110		Bw = 20 Hz to 20 kHz		50	100	μV
SVR	Supply voltage rejection	f = 100 Hz; V _r = 1 Vrms	50	70	-	dB
f _{ch}	High cut-off frequency	$P_{O} = 0.5 \text{ W}$	100	300	-	kHz
R_i	Input impedance	-	70	100	130	kΩ
C _T	Cross talk	f = 1 kHz P _O = 4 W f = 10 kHz P _O = 4 W	60	70 60	-	dB dB
	Ot and the comment of	V _{St-by} = 1.2 V	-	-	20	μΑ
I_{SB}	Standby current consumption	$V_{St-by} = 0$	-	-	10	μΑ
I _{pin5}	Standby pin current	V _{St-by} = 1.2 V to 2.6 V	-	-	±1	μΑ
V _{SB out}	Standby out threshold voltage	(Amp: ON)	2.6	-	-	V
V _{SB in}	Standby in threshold voltage	(Amp: OFF)	-	-	1.2	V
A _M	Mute attenuation	P _{Oref} = 4 W	80	90	-	dB
V _{M out}	Mute out threshold voltage	(Amp: Play)	2.6	-	-	V
$V_{M in}$	Mute in threshold voltage	(Amp: Mute)	-	-	1.2	V
V _{AM in}	V _S automute threshold	(Amp: Mute) Att ≥ 80 dB; P _{Oref} = 4 W	6.7	7	-	V
		(Amp: Play) Att < 0.1 dB; P _O = 0.5 W	-	7.5	8	V
lpin23	Muting pin current	V _{MUTE} = 1.2 V (Sourced current)	7	12	18	μΑ
		V _{MUTE} = 2.6 V	-5	-	18	μΑ
Clipping o	letector					
CD _{LK}	Clip detector high leakage current	Cd off	-	0	1	μΑ
CD _{SAT}	Clip detector saturation voltage	DC On; I _{CD} = 1 mA	-	0.2	0.4	V
CD _{THD}	Clip detector THD level	-	-	2	-	%
	i e e e e e e e e e e e e e e e e e e e	i .	1	L	L	

^{1.} Saturated square wave output.

3.3 Electrical characteristic curves

Figure 4. Quiescent current vs. supply voltage

Figure 5. Output power vs. supply voltage $(R_L = 4 \Omega)$

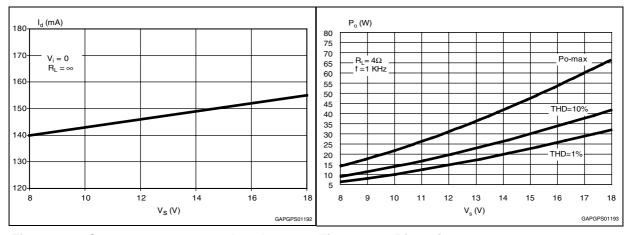


Figure 6. Output power vs. supply voltage $(R_L = 2 \Omega)$

Figure 7. Distortion vs. output power $(R_L = 4 \Omega)$

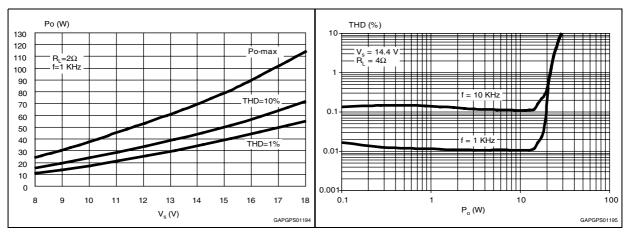


Figure 8. Distortion vs. output power $(R_L = 2 \Omega)$

Figure 9. Distortion vs. frequency ($R_L = 4 \Omega$)

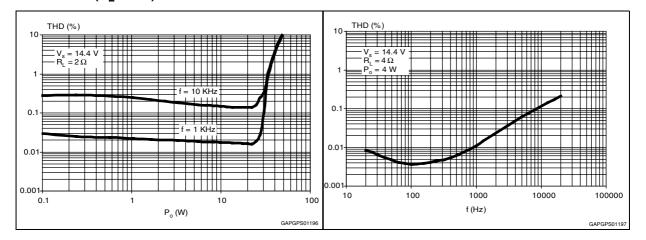


Figure 10. Distortion vs. frequency ($R_L = 2 \Omega$) Figure 11. Crosstalk vs. frequency

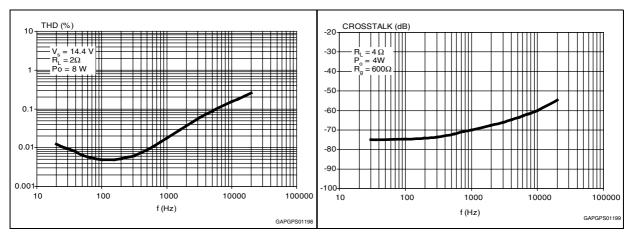


Figure 12. Supply voltage rejection vs. frequency

Figure 13. Output attenuation vs. supply voltage

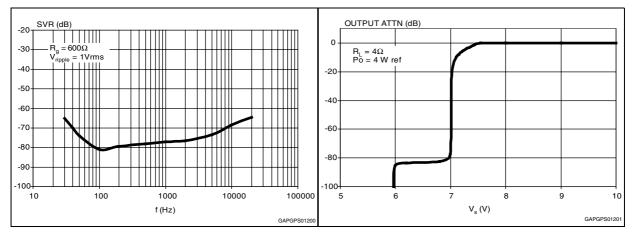
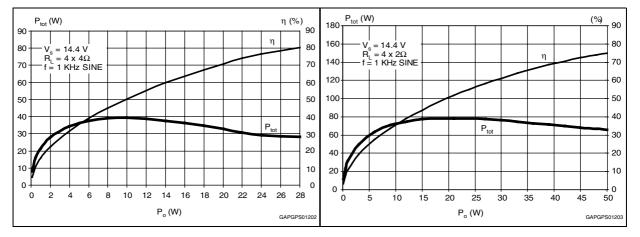


Figure 14. Power dissipation and efficiency vs. output power ($R_1 = 4 \Omega$, SINE)

Figure 15. Power dissipation and efficiency vs. output power ($R_1 = 2 \Omega$, SINE)



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Figure 16. Power dissipation vs. output power Figure 17. Power dissipation vs. output power ($R_L = 4 \Omega$, audio program simulation) ($R_L = 2 \Omega$, audio program simulation)

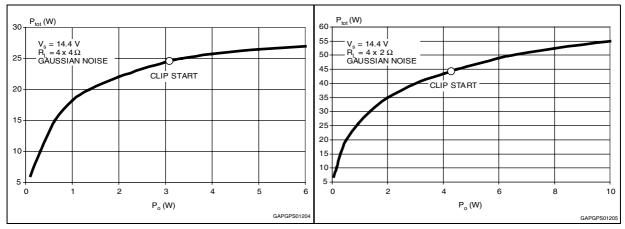
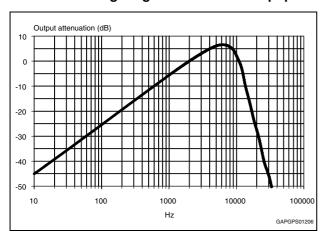


Figure 18. ITU R-ARM frequency response, weighting filter for transient pop



Application hints TDA7851L

4 Application hints

4.1 SVR

Besides its contribution to the ripple rejection, the SVR capacitor governs the turn ON/OFF time sequence and, consequently, plays an essential role in the pop optimization during ON/OFF transients. To conveniently serve both needs, **its minimum recommended value** is 10 µF.

4.2 Input stage

The TDA7851L's inputs are ground-compatible and support very high input signals $(\pm 8 \text{ Vpk})$ without any performances degradation.

If the standard value for the input capacitors (0.1 μ F) is adopted, the low frequency cut-off will amount to 16 Hz.

The input capacitors should be 1/4 of the capacitor connected to AC-GND pin for optimum pop performances.

4.3 Standby and muting

Standby and muting facilities are both CMOS-compatible. In absence of true CMOS ports or microprocessors, a direct connection to Vs of these two pins is admissible but a 470 k Ω equivalent resistance should be present between the power supply and the muting and stand-by pins.

R-C cells have always to be used in order to smooth down the transitions from preventing any audible transient noises.

About the standby, the time constant to be assigned in order to obtain a virtually pop-free transition has to be slower than 2.5 V/ms.

4.4 Heatsink definition

Under normal usage (4 Ω speakers) the heatsink's thermal requirements have to be deduced from *Figure 16*, which reports the simulated power dissipation when real music/speech programmes are played out. Noise with gaussian-distributed amplitude was employed for this simulation. Based on that, frequent clipping occurrence (worst-case) will cause $P_{diss} = 26$ W. Assuming $T_{amb} = 70^{\circ}$ C and $T_{CHIP} = 150$ °C as boundary conditions, the heatsink's thermal resistance should be approximately 2 °C/W. This would avoid any thermal shutdown occurrence even after long-term and full-volume operation.

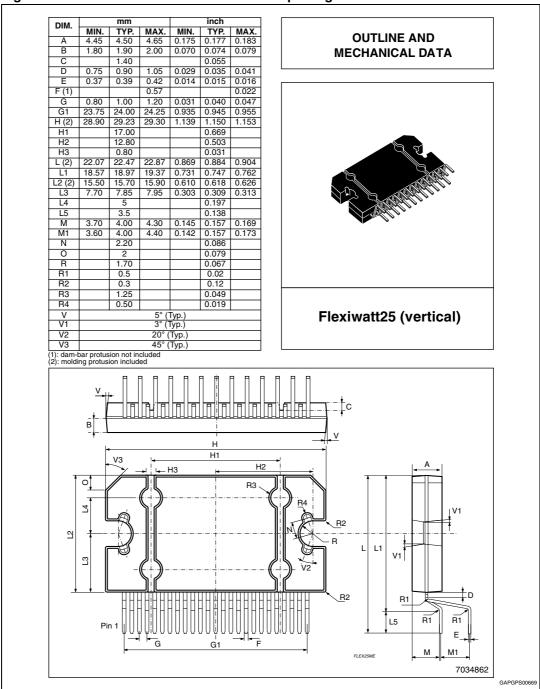
TDA7851L Package information

5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com.

ECOPACK® is an ST trademark.

Figure 19. Flexiwatt25 mechanical data and package dimensions



Revision history TDA7851L

6 Revision history

Table 5. Document revision history

Date	Revision	Changes
23-Nov-2011	1	Initial release.

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