

www.ti.com SCES770-DECEMBER 2008

8-BIT DUAL-SUPPLY BUS TRANSCEIVER WITH CONFIGURABLE VOLTAGE TRANSLATION AND 3-STATE OUTPUTS

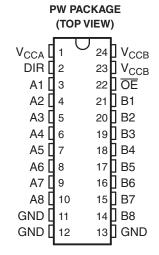
FEATURES

- Control Inputs V_{IH}/V_{IL} Levels Are Referenced to V_{CCA} Voltage
- V_{CC} Isolation Feature If Either V_{CC} Input Is at GND, All Are in the High-Impedance State
- Fully Configurable Dual-Rail Design Allows Each Port to Operate Over the Full 1.65-V to 5.5-V Power-Supply Range
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Protection Exceeds JESD 22
 - 4000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)

SUPPORTS DEFENSE, AEROSPACE, AND MEDICAL APPLICATIONS

- Controlled Baseline
- One Assembly/Test Site
- One Fabrication Site
- Available in Military (–55°C/125°C)
 Temperature Range⁽¹⁾
- Extended Product Life Cycle
- Extended Product-Change Notification
- Product Traceability

(1) Additional temperature ranges are available - contact factory



DESCRIPTION/ORDERING INFORMATION

This 8-bit noninverting bus transceiver uses two separate configurable power-supply rails. The SN74LVC8T245 is optimized to operate with V_{CCA} and V_{CCB} set at 1.65 V to 5.5 V. The A port is designed to track V_{CCA} . V_{CCA} accepts any supply voltage from 1.65 V to 5.5 V. The B port is designed to track V_{CCB} . V_{CCB} accepts any supply voltage from 1.65 V to 5.5 V. This allows for universal low-voltage bidirectional translation between any of the 1.8-V, 2.5-V, 3.3-V, and 5.5-V voltage nodes.

ORDERING INFORMATION(1)

T _A	PACKAGE ⁽	2)	ORDERABLE PART NUMBER	TOP-SIDE MARKING	
-55°C to 125°C	TSSOP – PW	Tape and reel	SN74LVC8T245MPWREP	NH245MEP	

- (1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI Web site at www.ti.com.
- (2) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



SCES770-DECEMBER 2008 www.ti.com

DESCRIPTION/ORDERING INFORMATION (CONTINUED)

The SN74LVC8T245 is designed for asynchronous communication between two data buses. The logic levels of the direction-control (DIR) input and the output-enable (\overline{OE}) input activate either the B-port outputs or the A-port outputs or place both output ports into the high-impedance mode. The device transmits data from the A bus to the B bus when the B-port outputs are activated, and from the B bus to the A bus when the A-port outputs are activated. The input circuitry on both A and B ports is always active and must have a logic HIGH or LOW level applied to prevent excess I_{CC} and I_{CCZ} .

The SN74LVC8T245 is designed so that the control pins (DIR and \overline{OE}) are supplied by V_{CCA}.

This device is fully specified for partial-power-down applications using I_{off}. The I_{off} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The V_{CC} isolation feature ensures that if either V_{CC} input is at GND, all outputs are in the high-impedance state.

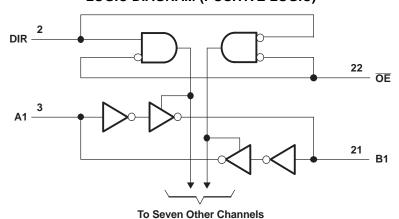
To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to V_{CC} through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

FUNCTION TABLE⁽¹⁾ (EACH 8-BIT SECTION)

CONTRO	L INPUTS	OUTPUT (CIRCUITS	ODEDATION
ŌĒ	DIR	A PORT	B PORT	OPERATION
L	L	Enabled	Hi-Z	B data to A bus
L	Н	Hi-Z	Enabled	A data to B bus
Н	Χ	Hi-Z	Hi-Z	Isolation

(1) Input circuits of the data I/Os are always active.

LOGIC DIAGRAM (POSITIVE LOGIC)



Submit Documentation Feedback

Copyright © 2008, Texas Instruments Incorporated



www.ti.com SCES770-DECEMBER 2008

Absolute Maximum Ratings(1)

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V_{CCA}	Supply voltage range		-0.5	6.5	V
		I/O ports (A port)	-0.5	6.5	
V_{I}	Input voltage range (2)	I/O ports (B port)	-0.5	6.5	V
		Control inputs	-0.5	6.5	
\/	Voltage range applied to any output	A port	-0.5	6.5	V
Vo	in the high-impedance or power-off state (2)	B port	-0.5	6.5	V
\/	Voltage range applied to any output in the high or low state (2)(3)	A port	-0.5 V	_{CCA} + 0.5	V
Vo	voltage range applied to any output in the high or low state ()	B port	-0.5 V	CCB + 0.5	V
I _{IK}	Input clamp current	V _I < 0		-50	mA
I _{OK}	Output clamp current	V _O < 0		-50	mA
Io	Continuous output current			±50	mA
	Continuous current through each V _{CCA} , V _{CCB} , and GND			±100	mA
θ_{JA}	Package thermal impedance (4)			88	°C/W
T _{stg}	Storage temperature range		-65	150	°C

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

The output positive-voltage rating may be exceeded up to 6.5 V maximum if the output current rating is observed.

The package thermal impedance is calculated in accordance with JESD 51-7.



SCES770-DECEMBER 2008 www.ti.com

Recommended Operating Conditions (1)(2)(3)(4)

			V _{CCI}	V _{cco}	MIN	MAX	UNIT
V_{CCA}	Cupply voltogo				1.65	5.5	V
V _{CCB}	Supply voltage				1.65	5.5	V
			1.65 V to 1.95 V		V _{CCI} × 0.65		
. /	High-level	Data inputs ⁽⁵⁾	2.3 V to 2.7 V		1.7		V
V_{IH}	input voltage	Data inputs (7)	3 V to 3.6 V		2		V
			4.5 V to 5.5 V		$V_{CCI} \times 0.7$		
			1.65 V to 1.95 V			V _{CCI} × 0.35	
.,	Low-level	Data inputs ⁽⁵⁾	2.3 V to 2.7 V			0.7	V
V_{IL}	input voltage	Data inputs (9)	3 V to 3.6 V			0.8	V
			4.5 V to 5.5 V			V _{CCI} × 0.3	
			1.65 V to 1.95 V		V _{CCA} × 0.65		
V_{IH}	High-level	Control inputs	2.3 V to 2.7 V		1.7		V
	input voltage	(referenced to V _{CCA}) ⁽⁶⁾	3 V to 3.6 V		2		
			4.5 V to 5.5 V		V _{CCA} × 0.7		
			1.65 V to 1.95 V			V _{CCA} × 0.35	
V_{IL}	Low-level	Control inputs	2.3 V to 2.7 V			0.7	V
- 12	input voltage	(referenced to V _{CCA}) ⁽⁶⁾	3 V to 3.6 V			0.8	
			4.5 V to 5.5 V			$V_{CCA} \times 0.3$	
VI	Input voltage	Control inputs			0	5.5	V
\/	Input/output	Active state			0	V _{cco}	V
V _{I/O}	voltage	3-State			0	5.5	V
				1.65 V to 1.95 V		-4	
	High-level output	ourront		2.3 V to 2.7 V		-8	mA
I _{OH}	r light-level output	Current		3 V to 3.6 V		-24	ША
				4.5 V to 5.5 V		-32	
				1.65 V to 1.95 V		4	
	Low-level output	ourront		2.3 V to 2.7 V		8	mA
l _{OL}	Low-level output t	current		3 V to 3.6 V		24	ША
				4.5 V to 5.5 V		32	
			1.65 V to 1.95 V			20	
Δt/Δν	Input transition	Data inputs	2.3 V to 2.7 V			20	ns/V
ΔVΔV	rise or fall rate	Data Iliputs	3 V to 3.6 V			10	115/ V
			4.5 V to 5.5 V			5	
T _A	Operating free-air	temperature			-55	125	°C

 V_{CCI} is the V_{CC} associated with the data input port.

Submit Documentation Feedback

Copyright © 2008, Texas Instruments Incorporated

 V_{CCO} is the V_{CC} associated with the output port.

All unused or driven (floating) data inputs (I/Os) of the device must be held at logic HIGH or LOW (preferably V_{CCI} or GND) to ensure proper device operation and minimize power. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.

All unused control inputs must be held at V_{CCA} or GND to ensure proper device operation and minimize power comsumption. For V_{CCI} values not specified in the data sheet, V_{IH} min = V_{CCI} × 0.7 V, V_{IL} max = V_{CCI} × 0.3 V. For V_{CCA} values not specified in the data sheet, V_{IH} min = V_{CCA} × 0.7 V, V_{IL} max = V_{CCA} × 0.3 V.



www.ti.com SCES770-DECEMBER 2008

Electrical Characteristics (1)(2)

over recommended operating free-air temperature range (unless otherwise noted)

PARA	AMETER	TEST CONDIT	TONS	V _{CCA}	V _{CCB}	MIN TYP MAX	MIN MAX	UNIT
		$I_{OH} = -100 \mu A$,	$V_I = V_{IH}$	1.65 V to 4.5 V	1.65 V to 4.5 V		V _{CCO} - 0.1	
		$I_{OH} = -4 \text{ mA},$	$V_I = V_{IH}$	1.65 V	1.65 V		1.2	
V_{OH}		$I_{OH} = -8 \text{ mA},$	$V_I = V_{IH}$	2.3 V	2.3 V		1.9	V
		$I_{OH} = -24 \text{ mA},$	$V_I = V_{IH}$	3 V	3 V		2.4	
		$I_{OH} = -32 \text{ mA},$	$V_I = V_{IH}$	4.5 V	4.5 V		3.8	
		$I_{OL} = 100 \mu A$,	$V_I = V_{IL}$	1.65 V to 4.5 V	1.65 V to 4.5 V		0.1	
		$I_{OL} = 4 \text{ mA},$	$V_I = V_{IL}$	1.65 V	1.65 V		0.45	
V_{OL}		$I_{OL} = 8 \text{ mA},$	$V_I = V_{IL}$	2.3 V	2.3 V		0.3	V
		I _{OL} = 24 mA,	$V_I = V_{IL}$	3 V	3 V		0.55	
		I _{OL} = 32 mA,	$V_I = V_{IL}$	4.5 V	4.5 V		0.55	
I _I	DIR	$V_I = V_{CCA}$ or GND		1.65 V to 5.5 V	1.65 V to 5.5 V	±1	±2	μΑ
	A or B	\\ -=\\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	,	0 V	0 to 5.5 V	±1	±6	^
l _{off}	port	V_I or $V_O = 0$ to 5.5 \	/	0 to 5.5 V	0 V	±1	±6	μΑ
l _{OZ}	A or B port	$\frac{V_O}{OE} = V_{CCO}$ or GND, OE = V_{IH}		1.65 V to 5.5 V	1.65 V to 5.5 V	±1	±2	μΑ
				1.65 V to 5.5 V	1.65 V to 5.5 V		15	
I_{CCA}		$V_I = V_{CCI}$ or GND,	$I_O = 0$	5 V	0 V		15	μΑ
				0 V	5 V		-2	
				1.65 V to 5.5 V	1.65 V to 5.5 V		15	
I _{CCB}		$V_I = V_{CCI}$ or GND,	$I_O = 0$	5 V	0 V		-2	μΑ
				0 V	5 V		15	
I _{CCA} +	I _{CCB}	$V_I = V_{CCI}$ or GND,	I _O = 0	1.65 V to 5.5 V	1.65 V to 5.5 V		25	μΑ
	A port	One A port at V _{CCA} DIR at V _{CCA} , B port	- 0.6 V, = open				50	
ΔI _{CCA}	DIR	DIR at V _{CCA} - 0.6 \ B port = open, A port at V _{CCA} or GI		3 V to 5.5 V	3 V to 5.5 V		50	μΑ
ΔI _{CCB}	B port	One B port at V _{CCB} DIR at GND, A port		3 V to 5.5 V	3 V to 5.5 V		50	μΑ
Ci	Control inputs	$V_I = V_{CCA}$ or GND		3.3 V	3.3 V	4	5	pF
C _{io}	A or B	$V_O = V_{CCA/B}$ or GNE)	3.3 V	3.3 V	8.5	10	pF

 $[\]begin{array}{ll} \hbox{(1)} & V_{CCO} \text{ is the } V_{CC} \text{ associated with the output port.} \\ \hbox{(2)} & V_{CCI} \text{ is the } V_{CC} \text{ associated with the input port.} \\ \end{array}$

SCES770-DECEMBER 2008 www.ti.com

Switching Characteristics

over recommended operating free-air temperature range, $V_{CCA} = 1.8 \text{ V} \pm 0.15 \text{ V}$ (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CCB} = ± 0.1		V _{CCB} = ± 0.2		V _{CCB} = ± 0.	= 3.3 V 3 V	V _{CCB} ± 0.		UNIT
	(INFOT)	(001F01)	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	А	В	1.7	25.9	1.3	13.2	1	11.4	0.8	11.1	ns
t _{PHL}	Λ		1.7	25.5	1.5	13.2		11.4	0.0	11.1	113
t _{PLH}	В	А	0.9	28.8	0.8	27.6	0.7	27.4	0.7	27.4	ns
t _{PHL}	J.	A	0.9	20.0	0.0	21.0	0.7	21.4	0.7	21.4	113
t _{PHZ}	ŌĒ	Α	1.5	33.6	1.5	33.4	1.5	33.3	1.4	33.2	ns
t _{PLZ}	OL	Α	1.5	33.0	1.5	33.4	1.0	55.5	1.4	33.2	113
t _{PHZ}	ŌĒ	В	2.4	36.2	1.9	17.1	1.7	16	1 2	14.3	ns
t _{PLZ}	OL		2.4	30.2	1.9	17.1	1.7	10	1.5	14.5	113
t _{PZH}	ŌĒ	Α	0.4	28	0.4	27.8	0.4	27.7	0.4	27.7	ns
t _{PZL})L	Α	0.4	20	0.4	21.0	0.4	21.1	0.4	21.1	115
t _{PZH}	ŌĒ	В	1.8	40	1.5	20	1.2	16.6	0.9	14.8	ns
t _{PZL}	OL .	٥	1.0	40	1.5	20	1.2	10.0	0.9	14.0	115

Switching Characteristics

over recommended operating free-air temperature range, V_{CCA} = 2.5 V \pm 0.2 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)		V _{CCB} = 1.8 V ± 0.15 V		V _{CCB} = 2.5 V ± 0.2 V		3.3 V 3 V	V _{CCB} = 5 V ± 0.5 V		UNIT
	(INFOT)	(0011 01)	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	A	В	1.5	25.4	1.2	13	0.8	10.2	0.6	8.8	ns
t _{PHL}	^	В	1.5	25.4	1.2	13	0.0	10.2	0.0	0.0	115
t _{PLH}	В	A	1.2	13.3	1	13.1	1	12.9	0.9	12.8	ns
t _{PHL}	В	A	1.2	13.3	-	13.1	'	12.9	0.9	12.0	115
t _{PHZ}	ŌĒ	A	1.4	13	1.4	13	1.4	13	1.4	13	ns
t _{PLZ}	OL	Α	1.4	13	1.4	13	1.4	13	1.4	13	115
t_{PHZ}	ŌĒ	В	2.3	33.6	1.8	15	1.7	14.3	0.9	10.9	ns
t _{PLZ}	OL	В	2.3	33.0	1.0	13	1.7	14.3	0.9	10.9	115
t_{PZH}	ŌĒ	A	1	17.2	1	17.3	1	17.2	1	17.3	ns
t _{PZL}	OE	^	'	17.2	ı	17.3		17.2		17.3	115
t _{PZH}	ŌĒ	D	1.7	32.2	1.5	18.1	1.2	14.1	1	11.2	ns
t _{PZL}	OE .	В	1.7	32.2	1.5	10.1	1.2	14.1	'	11.2	115

Submit Documentation Feedback



www.ti.com SCES770-DECEMBER 2008

Switching Characteristics

over recommended operating free-air temperature range, $V_{CCA} = 3.3 \text{ V} \pm 0.3 \text{ V}$ (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CCB} = 1 ± 0.15		V _{CCB} = ± 0.2		V _{CCB} = ± 0.		V _{CCB} ± 0.		UNIT
	(INFOT)	(001F01)	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	^	В	1.5	25.2	1.1	12.8	0.8	10.3	0.5	10.4	ns
t _{PHL}	Α	В	1.5	25.2	1.1	12.0	0.0	10.3	0.5	10.4	115
t _{PLH}	В	А	0.8	11.2	0.8	10.2	0.7	10.1	0.6	10	ns
t _{PHL}	В	^	0.0	11.2	0.0	10.2	0.7	10.1	0.0	10	113
t _{PHZ}	ŌĒ	А	1.6	12.2	1.6	12.2	1.6	12.2	1.6	12.2	ns
t _{PLZ}	OL	^	1.0	12.2	1.0	12.2	1.0	12.2	1.0	12.2	115
t _{PHZ}	ŌĒ	В	2.1	33	1.7	14.3	1.5	12.6	0.8	10.3	ns
t _{PLZ}	OL	В	2.1	33	1.7	14.5	1.0	12.0	0.0	10.5	113
t _{PZH}	ŌĒ	А	0.8	14.1	0.8	13.6	0.8	13.2	0.8	13.6	ns
t _{PZL}	OE	A	0.8	14.1	0.6	13.0	0.0	13.2	0.0	13.0	115
t _{PZH}	ŌĒ	В	1.8	31.7	1.4	18.4	1.1	12.9	0.9	10.9	ns
t _{PZL}	OL	В	1.0	31.7	1.4	10.4	1.1	12.9	0.9	10.9	115

Switching Characteristics

over recommended operating free-air temperature range, $V_{CCA} = 5 \text{ V} \pm 0.5 \text{ V}$ (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = ± 0.1		V _{CC} = ± 0.	2.5 V 2 V	V _{CC} = ± 0.		V _{CC} = ± 0.		UNIT
	(INFOT)	(001F01)	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	^	В	1.5	25.4	1	12.8	0.7	10	0.4	8.2	ns
t _{PHL}	Α	ם	1.5	25.4		12.0	0.7	10	0.4	0.2	115
t _{PLH}	В	Α	0.7	11	0.4	8.8	0.3	8.5	0.3	8.3	ns
t _{PHL}	В	٨	0.7	11	0.4	0.0	0.3	0.5	0.3	0.3	115
t _{PHZ}	ŌĒ	А	0.3	9.4	0.3	9.4	0.3	9.4	0.3	9.4	ns
t_{PLZ}	OE	A	0.3	9.4	0.3	9.4	0.3	9.4	0.3	9.4	115
t _{PHZ}	ŌĒ	В	2	32.7	1.6	13.7	1.4	12	0.7	9.7	ns
t _{PLZ}	OL	ם		32.1	1.0	13.7	1.4	12	0.7	9.1	115
t _{PZH}	ŌĒ	Α	0.7	10.9	0.7	10.9	0.7	10.9	0.7	10.9	ns
t _{PZL}	OL	٨	0.7	10.9	0.7	10.9	0.7	10.9	0.7	10.9	115
t _{PZH}	ŌĒ	В	1.5	31.6	1.3	18.4	1	13.7	0.9	10.7	nc
t _{PZL}	OE		1.5	31.0	1.3	10.4	ı	13.7	0.9	10.7	ns

Operating Characteristics

 $T_A = 25$ °C

	PARAMETER	TEST CONDITIONS	V _{CCA} = V _{CCB} = 1.8 V	V _{CCA} = V _{CCB} = 2.5 V	V _{CCA} = V _{CCB} = 3.3 V	V _{CCA} = V _{CCB} = 5 V	UNIT
C _{pdA} ⁽¹⁾	A-port input, B-port output		2	2	2	3	
C _{pdA} ` '	B-port input, A-port output	$C_L = 0$,	12	13	13	16	
c (1)	A-port input, B-port output	f = 10 MHz, $t_r = t_f = 1 \text{ ns}$	13	13	14	16	pF
C _{pdB} ⁽¹⁾	B-port input, A-port output		2	2	2	3	

(1) Power dissipation capacitance per transceiver

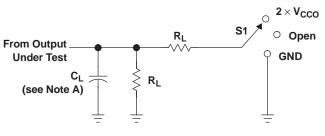
TEXAS INSTRUMENTS

 V_{CCA}

CCA/2

SCES770-DECEMBER 2008 www.ti.com

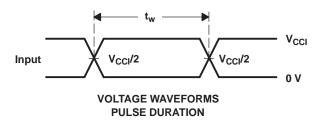
PARAMETER MEASUREMENT INFORMATION



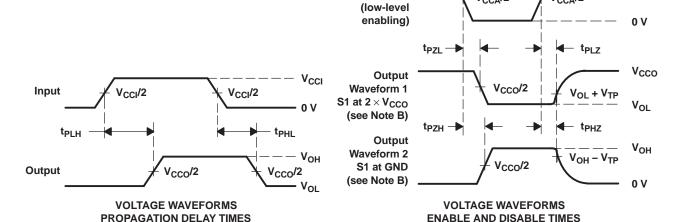
TEST	S1
t _{pd}	Open
t _{PLZ} /t _{PZL}	$2 \times V_{CCO}$
t _{PHZ} /t _{PZH}	GND

LOAD CIRCUIT

V _{CCO}	CL	R _L	V _{TP}
1.8 V \pm 0.15 V	15 pF	2 k Ω	0.15 V
2.5 V \pm 0.2 V	15 pF	2 k Ω	0.15 V
3.3 V \pm 0.3 V	15 pF	2 k Ω	0.3 V
5 V \pm 0.5 V	15 pF	2 k Ω	0.3 V



V_{CCA}/2



Output Control

NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $Z_0 = 50 \Omega$, $dv/dt \geq$ 1 V/ns.
- D. The outputs are measured one at a time, with one transition per measurement.
- E. t_{PLZ} and t_{PHZ} are the same as t_{dis} .
- F. t_{PZL} and t_{PZH} are the same as t_{en}.
- G. t_{PLH} and t_{PHL} are the same as t_{pd} .
- H. V_{CCI} is the V_{CC} associated with the input port.
- I. V_{CCO} is the V_{CC} associated with the output port.
- J. All parameters and waveforms are not applicable to all devices.

Figure 1. Load Circuit and Voltage Waveforms

30-Jan-2012

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
SN74LVC8T245MPWREP	ACTIVE	TSSOP	PW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
V62/09615-01XE	ACTIVE	TSSOP	PW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

OTHER QUALIFIED VERSIONS OF SN74LVC8T245-EP:

Catalog: SN74LVC8T245

Automotive: SN74LVC8T245-Q1





30-Jan-2012

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects

PACKAGE MATERIALS INFORMATION

www.ti.com 28-Jan-2012

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

TAPE AND REEL INFORMATION

*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LVC8T245MPWREP	TSSOP	PW	24	2000	330.0	16.4	6.95	8.3	1.6	8.0	16.0	Q1

www.ti.com 28-Jan-2012



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LVC8T245MPWREP	TSSOP	PW	24	2000	346.0	346.0	33.0

PW (R-PDSO-G24)

PLASTIC SMALL OUTLINE



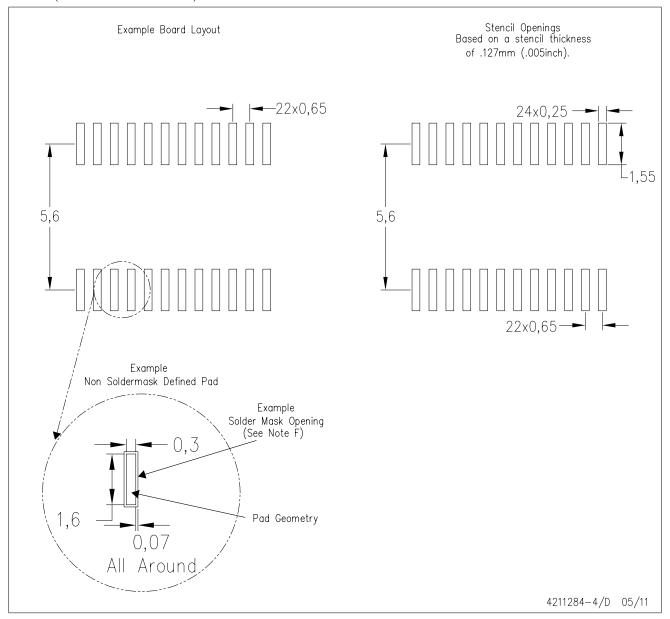
NOTES:

- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M—1994.
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
- E. Falls within JEDEC MO-153



PW (R-PDSO-G24)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate design.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

Applications

Automotive and Transportation www.ti.com/automotive

e2e.ti.com

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

		•	
Amplifiers	amplifier.ti.com	Communications and Telecom	www.ti.com/communications
Data Converters	dataconverter.ti.com	Computers and Peripherals	www.ti.com/computers
DLP® Products	www.dlp.com	Consumer Electronics	www.ti.com/consumer-apps
DSP	dsp.ti.com	Energy and Lighting	www.ti.com/energy
Clocks and Timers	www.ti.com/clocks	Industrial	www.ti.com/industrial
Interface	interface.ti.com	Medical	www.ti.com/medical
Logic	logic.ti.com	Security	www.ti.com/security
Power Mgmt	power.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video

RFID <u>www.ti-rfid.com</u>
OMAP Mobile Processors www.ti.com/omap

Products

Audio

Wireless Connectivity www.ti.com/wirelessconnectivity

www.ti.com/audio

TI E2E Community Home Page

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2012, Texas Instruments Incorporated