

The RF MOSFET Line 45W, 150MHz, 28V

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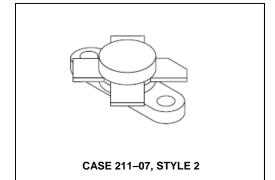
Designed primarily for wideband large—signal output and driver stages from **Product Image** 30–200 MHz.

N-Channel enhancement mode MOSFET

Guaranteed performance at 150 MHz, 28 Vdc

Output power = 45 W Power gain = 17 dB (min) Efficiency = 60% (min)

- Excellent thermal stability, ideally suited for Class A operation
- Facilitates manual gain control, ALC and modulation techniques
- 100% tested for load mismatch at all phase angles with 30:1 VSWR
- Low Crss − 8 pF @ VDS = 28 V
- Gold top metal

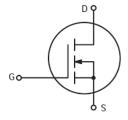


Typical data for power amplifier applications in industrial, commercial and amateur radio equipment

• Typical performance at 30 MHz, 28 Vdc

Output power = 30 W (PEP) Power gain = 20 dB (typ.) Efficiency = 50% (typ.) IMD(d3) (30 W PEP) -32 dB (typ.)

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Gate Voltage	VDSS	65	Vdc
Drain–Gate Voltage (R _{GS} = 1.0 M Ω)	V _{DGR}	65	Vdc
Gate-Source Voltage	VGS	±20	Adc
Drain Current — Continuous	ΙD	4.5	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	115 0.66	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C
Operating Junction Temperature	TJ	200	°C

THERMAL CHARACTERISTICS

Γ	Characteristic	Symbol	Max	Unit
Γ	Thermal Resistance, Junction to Case	R _e JC	1.52	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit			
OFF CHARACTERISTICS								
Drain–Source Breakdown Voltage (I _D = 50 mA, V _{GS} = 0)	V(BR)DSS	65	80	_	Vdc			
Zero Gate Voltage Drain Current (VGS = 0, VDS = 28 V)	IDSS	_	_	1.0	mAdc			
Gate-Source Leakage Current (VGS = 20 V, VDS = 0)	l _{GSS}	_	_	1.0	μAdc			

NOTE – <u>CAUTION</u> – MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.

- ADVANCED: Data Sheets contain information regarding a product M/A-COM Technology Solutions is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed.
- PRELIMINARY: Data Sheets contain information regarding a product MA-COM Technology Solutions has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.
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EL ECTRICAL	CHARACTERISTICS - continued (Tc = 25°C unless otherwise noted)
ELECTRICAL	CHARACIERISTICS - continued (10 = 25°C unless otherwise noted)

Symbol	Min	Тур	Max	Unit	
VGS(th)	1.5	2.5	4.5	Vdc	
VDS(on)	_	1.0	_	٧	
9fs	1.4	1.8	_	mhos	
•					
C _{iss}	_	60	_	pF	
Coss	_	70	_	pF	
C _{rss}	_	8	_	pF	
G _{ps}	17	19.5	_	dB	
η	60	70	_	%	
	No Degradation in Output Power				
G _{ps}	_	20	_	dB	
η	_	50	_	%	
IMD(d3)	_	-32	_	dB	
	VGS(th) VDS(on) 9fs Ciss Coss Gps η	VGS(th) 1.5 VDS(on) — 9fs 1.4 Ciss — Coss — Crss — Gps 17 η 60 No Gps — η —	VGS(th) 1.5 2.5 VDS(on) — 1.0 gfs 1.4 1.8 Ciss — 60 Coss — 70 Crss — 8 Gps 17 19.5 η 60 70 No Degradation Gps — 20 η — 50	VGS(th) 1.5 2.5 4.5 VDS(on) — 1.0 — gfs 1.4 1.8 — Ciss — 60 — Coss — 70 — Crss — 8 — Gps 17 19.5 — No Degradation in Output Pow Gps — 20 — η — 50 —	

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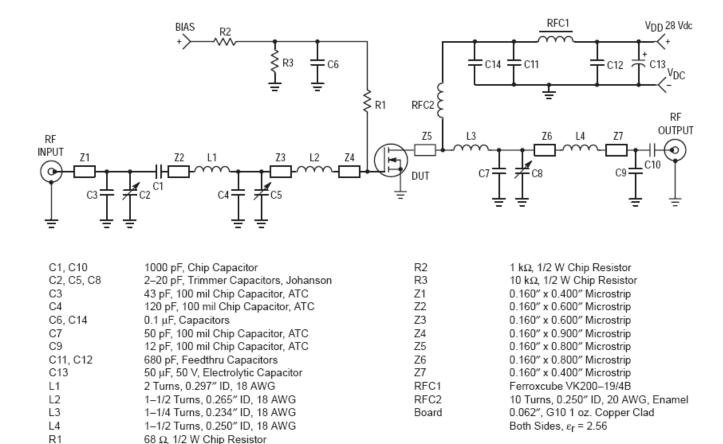


Figure 1. MRF171A 150 MHz Test Circuit

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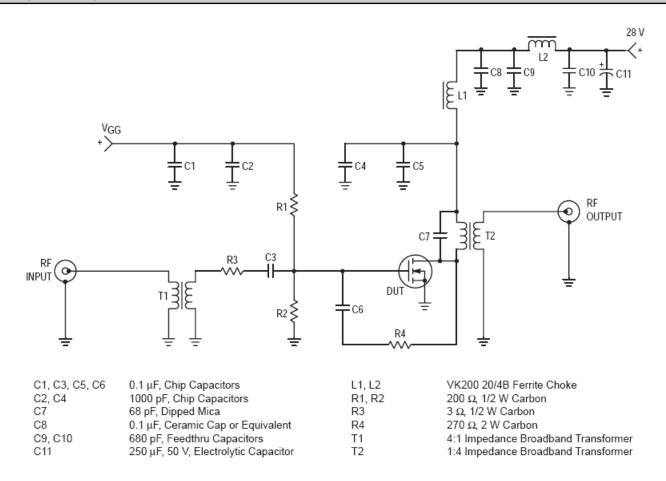


Figure 2. MRF171A 30 MHz Test Circuit

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TYPICAL CHARACTERISTICS

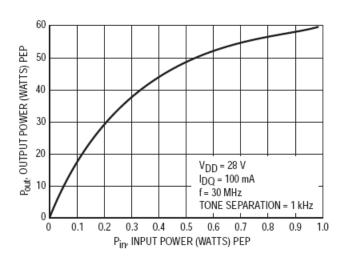


Figure 3. Output Power versus Input Power

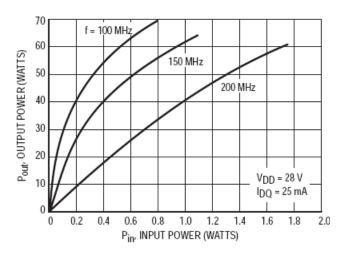


Figure 4. Output Power versus Input Power

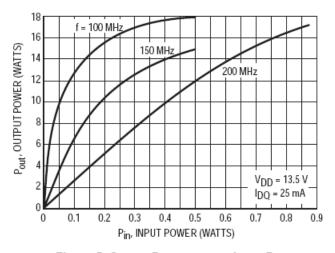


Figure 5. Output Power versus Input Power

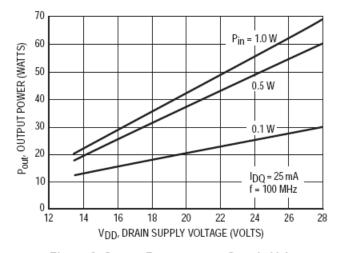
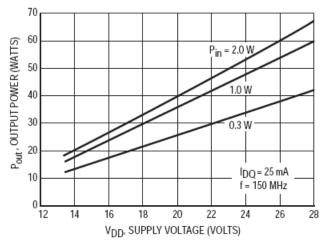


Figure 6. Output Power versus Supply Voltage

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80 70 Pin = 4.0 W P_{out}, OUTPUT POWER (WATTS) 3.0 W 60 2.0 W 50 40 30 20 $I_{DQ} = 25 \, \text{mA}$ 10 f = 200 MHz 14 20 24 26 12 V_{DD}, SUPPLY VOLTAGE (VOLTS)

Figure 7. Output Power versus Supply Voltage

Figure 8. Output Power versus Supply Voltage

TYPICAL CHARACTERISTICS

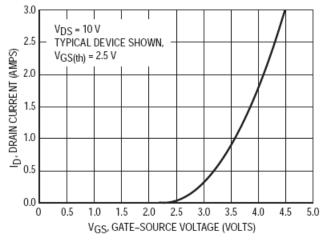


Figure 9. Drain Current versus Gate Voltage (Transfer Characteristics)

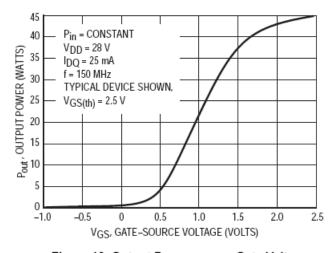


Figure 10. Output Power versus Gate Voltage

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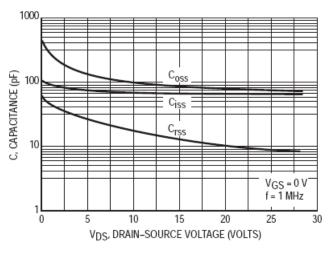
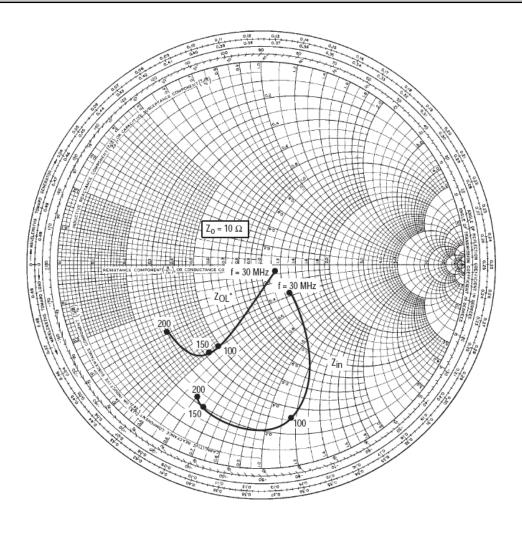


Figure 11. Capacitance versus Drain-Source Voltage

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V_{DD} = 28 V, I_{DO} = 25 mA, P_{out} = 45 W

f MHz	Z _{in} (1) Ω	Z _{OL} (2) Ω
30	12.8 – j3.6	11.5 – j0.99
100	3.1 – j11.6	4.9 – j4.9
150	2.0 - j6.5	4.2 – j4.9
200	2.2 – j6.0	3.0 – j2.9

(1) 68 Ω shunt resistor gate-to-ground.

(2) Z_{OL} = Conjugate of the optimum load impedance into which the device operates at a given output power, voltage and frequency.

Figure 12. Large-Signal Series Equivalent Input/Output Impedance

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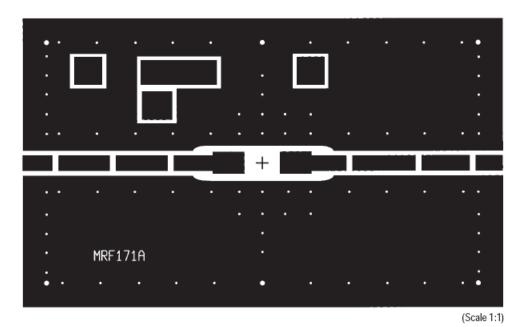


Figure 13. MRF171A Circuit Board Photo Master

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Table 1. Common Source S-Parameters (VDS = 12.5 V, ID = 0.5 A)

f	, S ₁₁		§ ₂₁		S ₁₂		s ₂₂	
MHz	S ₁₁	ф	S ₂₁	ф	S ₁₂	ф	\$ ₂₂	ф
30	0.801	-162	11.90	96	0.026	13	0.811	-166
40	0.809	-166	9.12	91	0.028	11	0.812	-171
50	0.810	-169	7.29	88	0.027	11	0.831	-172
60	0.808	-170	6.22	85	0.028	9	0.824	-174
70	0.814	-172	5.30	82	0.028	9	0.831	-176
80	0.811	-173	4.56	81	0.027	10	0.837	-175
90	0.811	-174	4.04	80	0.027	13	0.829	-174
100	0.814	-174	3.66	77	0.027	12	0.846	-176
110	0.812	-175	3.37	75	0.027	11	0.842	-177
120	0.816	-175	3.00	74	0.027	13	0.850	-176
130	0.816	-176	2.75	73	0.027	14	0.849	-175
140	0.817	-176	2.57	72	0.027	17	0.851	-176
150	0.821	-176	2.37	69	0.027	17	0.863	-177
160	0.820	-176	2.27	67	0.027	17	0.853	-177
170	0.821	-177	2.08	66	0.026	19	0.838	-177
180	0.824	-177	1.93	65	0.027	19	0.861	-177
190	0.825	-177	1.89	64	0.027	21	0.873	-177
200	0.830	-177	1.74	62	0.027	23	0.873	-178
210	0.831	-177	1.67	60	0.027	25	0.874	-177
220	0.831	-178	1.62	59	0.026	28	0.870	-178
230	0.836	-178	1.48	57	0.027	27	0.909	-179
240	0.836	-178	1.43	56	0.027	26	0.865	-180
250	0.839	-178	1.37	57	0.028	30	0.873	-178
260	0.844	-178	1.30	54	0.028	34	0.882	-179
270	0.842	-178	1.28	52	0.028	36	0.887	-180
280	0.845	-179	1.21	52	0.027	37	0.881	-180
290	0.849	-179	1.14	50	0.027	36	0.869	179
300	0.849	-179	1.12	50	0.029	39	0.852	-180
310	0.855	-179	1.06	49	0.029	42	0.891	-179
320	0.856	-179	1.03	46	0.030	43	0.889	180
330	0.856	-180	0.96	45	0.031	47	0.868	180
340	0.858	-180	0.96	46	0.030	47	0.888	179
350	0.860	180	0.93	44	0.031	49	0.875	-180
360	0.862	180	0.91	44	0.033	48	0.901	179
370	0.866	180	0.86	43	0.034	50	0.913	178
380	0.867	179	0.84	41	0.036	52	0.897	178
390	0.869	179	0.82	42	0.035	54	0.893	178
400	0.870	179	0.78	40	0.035	57	0.880	180
410	0.872	179	0.77	39	0.037	55	0.923	178
420	0.876	178	0.73	37	0.039	54	0.915	176
430	0.877	178	0.69	38	0.040	56	0.903	177
440	0.879	178	0.68	39	0.041	58	0.921	178

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Table 1. Common Source S-Parameters (VDS = 12.5 V, ID = 0.5 A) (continued)

f	S	11	Si	21	S-	12	S	22
MHz	S ₁₁	ф	\$ ₂₁	ф	S ₁₂	ф	S ₂₂	ф
450	0.882	177	0.68	36	0.040	61	0.926	178
460	0.884	177	0.65	36	0.041	59	0.937	175
470	0.886	177	0.62	35	0.041	60	0.896	176
480	0.885	176	0.62	33	0.044	61	0.907	176
490	0.886	176	0.61	32	0.046	63	0.907	176
500	0.887	176	0.59	31	0.047	65	0.916	175

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The RF MOSFET Line 45W, 150MHz, 28V

Table 2. Common Source S-Parameters (VDS = 28 V, ID = 0.5 A)

f	S ₁₁	ф	\$ ₂₁	ф	S ₁₂	ф	\$ ₂₂	ф
MHz	19111	Ψ	13211	Ψ	13121	Ψ	13221	Ψ
30	0.783	-152	17.10	100	0.025	17	0.730	-158
40	0.793	-158	13.20	94	0.027	13	0.730	-164
50	0.793	-162	10.50	90	0.027	12	0.754	-167
60	0.791	-165	9.00	87	0.027	11	0.746	-169
70	0.798	-167	7.68	83	0.026	10	0.760	-171
80	0.795	-169	6.63	82	0.026	10	0.770	-170
90	0.795	-170	5.85	80	0.026	12	0.760	-170
100	0.799	-170	5.30	77	0.026	10	0.779	-172
110	0.798	-171	4.86	75	0.026	11	0.775	-174
120	0.802	-172	4.35	74	0.025	13	0.785	-172
130	0.801	-172	3.97	72	0.025	14	0.788	-171
140	0.803	-173	3.70	71	0.025	15	0.791	-172
150	0.809	-173	3.42	68	0.025	14	0.808	-173
160	0.808	-173	3.27	66	0.025	15	0.796	-172
170	0.809	-174	2.99	65	0.024	18	0.783	-174
180	0.814	-174	2.77	63	0.025	19	0.809	-173
190	0.815	-175	2.71	62	0.024	21	0.820	-174
200	0.822	-175	2.49	60	0.024	22	0.826	-175
210	0.824	-175	2.37	57	0.024	24	0.836	-175
220	0.825	-175	2.23	57	0.024	26	0.807	-175
230	0.831	-176	2.08	56	0.024	29	0.839	-175
240	0.830	-176	2.00	54	0.024	29	0.818	-176
250	0.832	-176	1.92	55	0.024	33	0.828	-174
260	0.838	-176	1.81	53	0.024	35	0.829	-175
270	0.837	-176	1.79	50	0.025	37	0.834	-175
280	0.840	-177	1.69	50	0.025	39	0.832	-176
290	0.844	-177	1.60	48	0.025	39	0.836	-177
300	0.844	-177	1.55	48	0.025	44	0.814	-175
310	0.849	-178	1.48	47	0.026	46	0.848	-175
320	0.852	-178	1.43	44	0.027	45	0.855	-177
330	0.852	-178	1.35	43	0.028	48	0.833	-177
340	0.855	-178	1.32	44	0.028	49	0.861	-177
350	0.856	-178	1.29	41	0.029	53	0.842	-176

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Table 2. Common Source S-Parameters (V_{DS} = 28 V, I_{D} = 0.5 A) (continued)

f MHz	S ₁₁	ф	\$ ₂₁	ф	\$ ₁₂	ф	\$ ₂₂	ф	
360	0.859	-179	1.25	42	0.030	54	0.872	-178	
370	0.863	-179	1.18	39	0.030	55	0.886	-178	
380	0.864	-179	1.15	38	0.031	55	0.864	-178	
390	0.867	-179	1.12	39	0.032	57	0.862	-179	
400	0.869	-180	1.07	37	0.032	60	0.853	-177	
410	0.872	-180	1.05	35	0.035	60	0.898	-179	
420	0.876	180	1.00	34	0.036	60	0.889	180	
430	0.877	179	0.95	35	0.037	62	0.884	-179	
440	0.879	179	0.93	34	0.038	64	0.902	-179	
450	0.882	179	0.91	32	0.039	65	0.901	-180	
460	0.884	178	0.88	32	0.041	64	0.922	179	
470	0.885	178	0.84	32	0.040	66	0.877	179	
480	0.885	178	0.83	30	0.042	66	0.892	179	
490	0.886	177	0.81	29	0.044	68	0.891	179	
500	0.887	177	0.80	28	0.045	68	0.900	178	

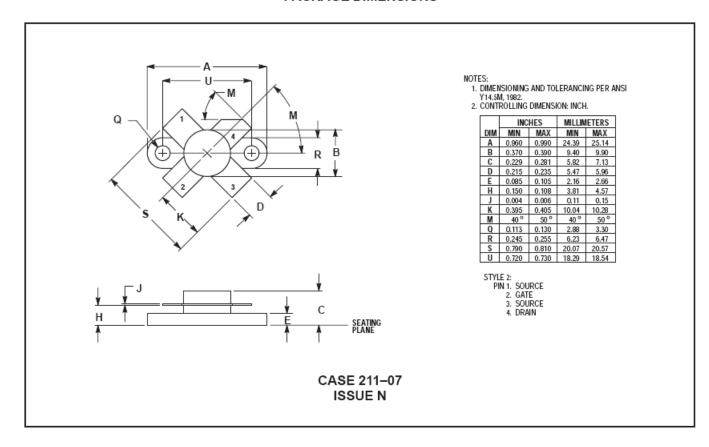
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