

STEREO AMPLIFIER AND DC VOLUME CONTROL FOR TV

- Stereo Circuit
- DC Volume Control
- 12dB Maximum Gain

DESCRIPTION

The TDA8199 is a monolithic integrated circuit in DIP8 package intented for TV applications.

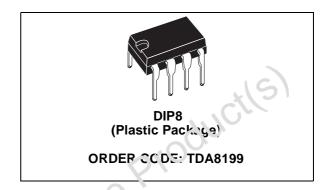


Figure 1. Pin Connections

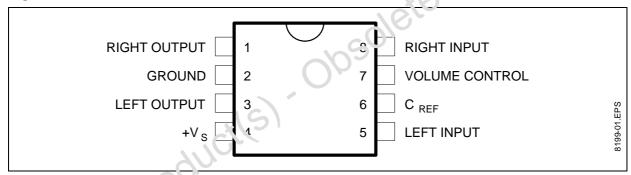
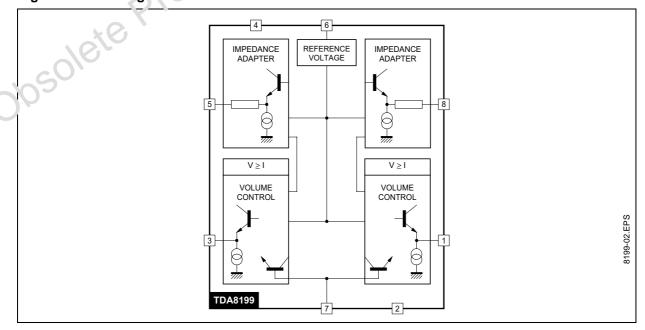


Figure 2. Block Diagram



September 2003

TDA8199

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _s	Supply Voltage (pin 1)	16	V
T _{stg}	Storage Temperature	- 55 to 125	°C
T _{oper}	Operating Ambient Temperature	0 to 70	°C

ELECTRICAL CHARACTERISTICS

Measured according to the following conditions, unless otherwise specified : $T_{amb} = 25^{oC}$, $V_S = +12V$.

	Parameter	Min.	Тур.	Max.	Uni
V _S	Supply Voltage	10.8	12	13.2	V
I _S	Supply Current (V _{IN} = 0, V _C = 0.5V)		21	28	m/
V _{REF}	Reference Voltage		6.9		V
V _i	Audio Input Amplitude		0.125	0.5	√ _R _N
THD1	Distortion for $V_I = 0.25 V_{RMS}$ at Max. Volume		0.35		%
THD2	Distortion for V _O = 2 V _{RMS}		71)	5	%
DK	DC Volume Control Range at V _I = 0.5 V _{RMS}	70	62		dE
Kmin	Output/Input Gain for Max. Volume (V _C = 5V)		12		dE
dK	Gain Difference between Channels at V _C = 5V		0		dE
C _C	Crosstalk between Channels ($R_L > 10k\Omega$ and $F = 1kHz$)		70		dE
R _I	Audio Input Resistance		22		kΩ
Ro	Audio Output Resistance		0.3	1	kΩ
	Output Noise Level at VC = 5V (weighted curve : DIN 5705)		300		μV _{RI}
	Volume Control Input Current (Pin 7) at V _C = f v		- 25		μA
	Volume Thermal Stability (K = 30dB, 0 < T _{amb} < 6 ^f / ₀ C)		0.04		dB/
	Volume Thermal Stability (IV = SSAB), 8 V Tamb V S S		0.04		<u> </u>
	Volume Thermal Stability (K = 30dB, 0 < T _{amb} < 6 ^f / ₂ °C)		0.04		

Figure 3. Gain versus Volume Control

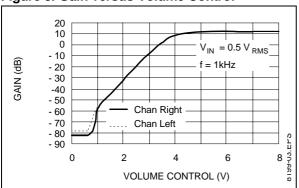


Figure 5. Distortion versus Volume Control

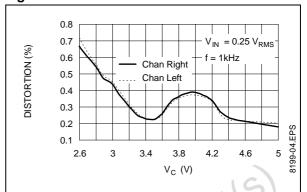


Figure 4. Distortion Rate versus Voltage Input

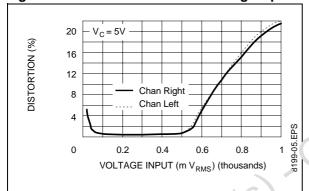


Figure 6. Supply Voltage Rejection

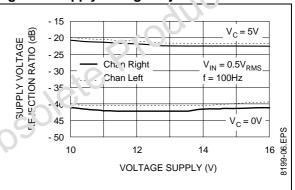
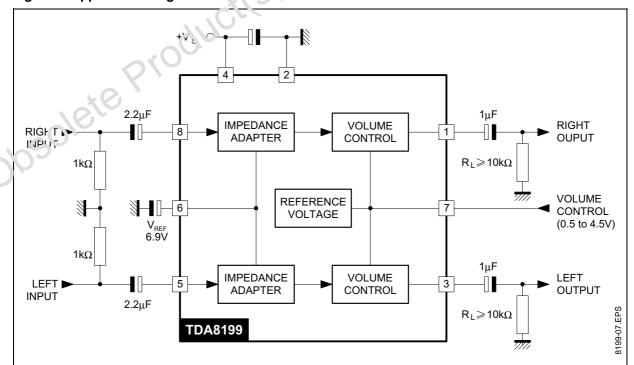


Figure 7. Application Diagram

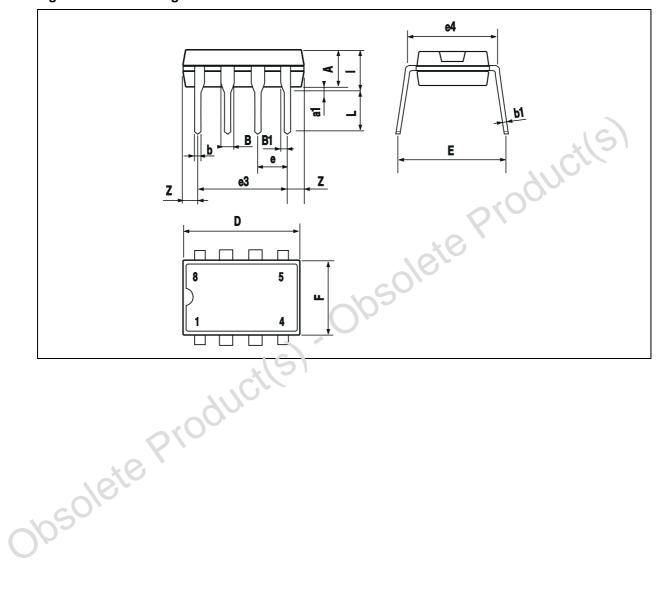


57

PACKAGE MECHANICAL DATA

8-PINS - PLASTIC DIP

Figure 8. 8-Pin Package



4/5

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infiringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication superseds and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without the express written approval of STMicroelectronics.

The ST logo is a registered trademark of STMicroelectronics

©2003 STMicroelectronics - All Rights Reserved.

Purchase of I^2C Components by STMicroelectronics conveys a license under the Philips I^2C Patent. Rights to use these components in an I^2C system is granted provided that the system conforms to the I^2C Standard Specification as defined by Philips.

STMicroelectronics Group of Companies

Australia - Brazil - China - Finland - France - Germany - Hong Kong - India - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain Sweden - Switzerland - United Kingdom - U.S.A.

http://www.st.com

57