

Features

- Improved E_{off} at elevated temperature
- Minimal tail current
- Low conduction losses

Applications

- Welding
- High frequency converters
- Power factor correction

Description

These devices are based on a new advanced planar technology concept to yield an IGBT with more stable switching performance (E_{off}) versus temperature, as well as lower conduction losses. The device is tailored to high switching frequency operation (over 100 kHz).

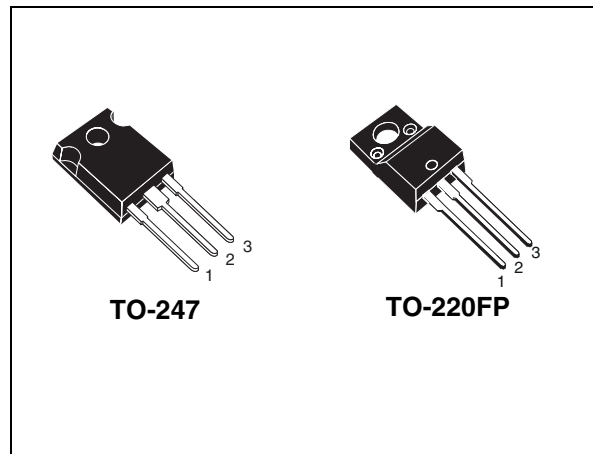


Figure 1. Internal schematic diagram

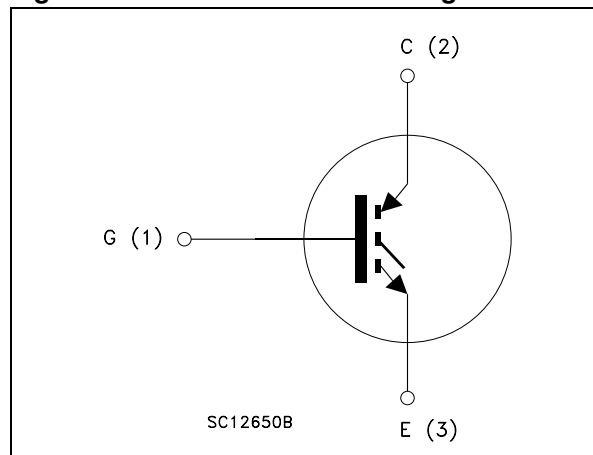


Table 1. Device summary

Order codes	Marking	Package	Packaging
STGF35HF60W	GF35HF60W	TO-220FP	Tube
STGW35HF60W	GW35HF60W	TO-247	

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TO-247	TO-220FP	
V _{CES}	Collector-emitter voltage (V _{GE} = 0)	600		V
I _C ⁽¹⁾	Continuous collector current at T _C = 25 °C	60	19	A
I _C ⁽¹⁾	Continuous collector current at T _C = 100 °C	35	12	A
I _{CP} ⁽²⁾	Pulsed collector current	150		A
I _{CL} ⁽³⁾	Turn-off latching current	80		A
V _{GE}	Gate-emitter voltage	± 20		V
P _{TOT}	Total dissipation at T _C = 25 °C	200	40	W
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t = 1 s; T _C = 25 °C)	2500		V
T _{stg}	Storage temperature	- 55 to 150		°C
T _j	Operating junction temperature			

1. Calculated according to the iterative formula:

$$I_C(T_C) = \frac{T_{j(max)} - T_C}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_C(T_C))}$$

2. Pulse width limited by maximum junction temperature and turn-off within RBSOA
 3. V_{CLAMP} = 80% (V_{CES}), V_{GE} = 15 V, R_G = 10 Ω, T_J = 150 °C

Table 3. Thermal data

Symbol	Parameter	Value		Unit
		TO-247	TO-220FP	
R _{thj-case}	Thermal resistance junction-case	0.63	3.1	°C/W
R _{thj-amb}	Thermal resistance junction-ambient	50	62.5	°C/W

2 Electrical characteristics

($T_J = 25\text{ °C}$ unless otherwise specified).

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage ($V_{GE} = 0$)	$I_C = 1\text{ mA}$	600			V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}, I_C = 20\text{ A}$		2	2.5	V
		$V_{GE} = 15\text{ V}, I_C = 20\text{ A}, T_J = 125\text{ °C}$		1.65		
$V_{GE(th)}$	Gate threshold voltage	$V_{CE} = V_{GE}, I_C = 1\text{ mA}$	3.75		5.75	V
I_{CES}	Collector cut-off current ($V_{GE} = 0$)	$V_{CE} = 600\text{ V}$			250	μA
		$V_{CE} = 600\text{ V}, T_J = 125\text{ °C}$			1	mA
I_{GES}	Gate-emitter leakage current ($V_{CE} = 0$)	$V_{GE} = \pm 20\text{ V}$			± 100	nA

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{ies} C_{oes} C_{res}	Input capacitance	$V_{CE} = 25\text{ V}, f = 1\text{ MHz},$ $V_{GE} = 0$	-	2400	-	μF
	Output capacitance			235		
	Reverse transfer capacitance			50		
Q_g Q_{ge} Q_{gc}	Total gate charge	$V_{CE} = 400\text{ V}, I_C = 20\text{ A},$ $V_{GE} = 15\text{ V},$ (see Figure 17)	-	140	-	nC
	Gate-emitter charge			13		
	Gate-collector charge			52		

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ t_r $(di/dt)_{on}$	Turn-on delay time	$V_{CC} = 400\text{ V}, I_C = 20\text{ A}$ $R_G = 10\ \Omega, V_{GE} = 15\text{ V},$ (see Figure 16)	-	30	-	ns
	Current rise time			15		
	Turn-on current slope			1650		
$t_{d(on)}$ t_r $(di/dt)_{on}$	Turn-on delay time	$V_{CC} = 400\text{ V}, I_C = 20\text{ A}$ $R_G = 10\ \Omega, V_{GE} = 15\text{ V},$ $T_J = 125\text{ °C}$ (see Figure 16)	-	30	-	ns
	Current rise time			15		
	Turn-on current slope			1600		

Table 6. Switching on/off (inductive load) (continued)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 400\text{ V}$, $I_C = 20\text{ A}$, $R_{GE} = 10\ \Omega$, $V_{GE} = 15\text{ V}$ (see Figure 16)	-	30	-	ns
$t_{d(off)}$	Turn-off delay time			175		ns
t_f	Current fall time			40		ns
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 400\text{ V}$, $I_C = 20\text{ A}$, $R_{GE} = 10\ \Omega$, $V_{GE} = 15\text{ V}$, $T_J = 125\text{ }^\circ\text{C}$ (see Figure 16)	-	50	-	ns
$t_{d(off)}$	Turn-off delay time			225		ns
t_f	Current fall time			70		ns

Table 7. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 400\text{ V}$, $I_C = 20\text{ A}$, $R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$, (see Figure 18)	-	290		μJ
E_{off}	Turn-off switching losses			185		μJ
E_{ts}	Total switching losses			475		μJ
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 400\text{ V}$, $I_C = 20\text{ A}$, $R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$, $T_J = 125\text{ }^\circ\text{C}$ (see Figure 18)	-	420	530	μJ
E_{off}	Turn-off switching losses			350		μJ
E_{ts}	Total switching losses			770		μJ

1. E_{on} is the turn-on losses when a typical diode is used in the test circuit in [Figure 18](#). If the IGBT is offered in a package with a co-pak diode, the co-pak diode is used as external diode. IGBTs and diode are at the same temperature (25 °C and 125 °C). E_{on} include diode recovery energy.

2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

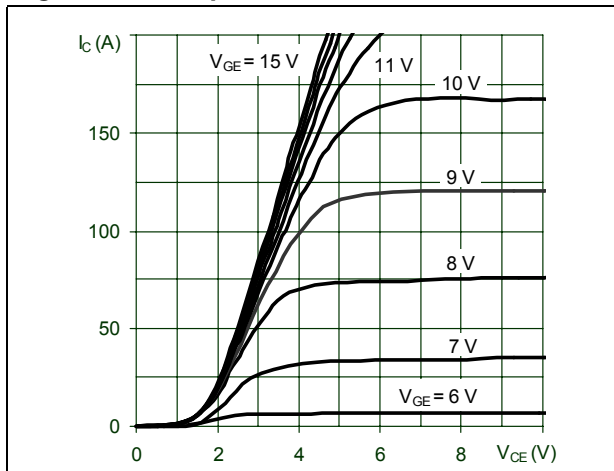


Figure 3. Transfer characteristics

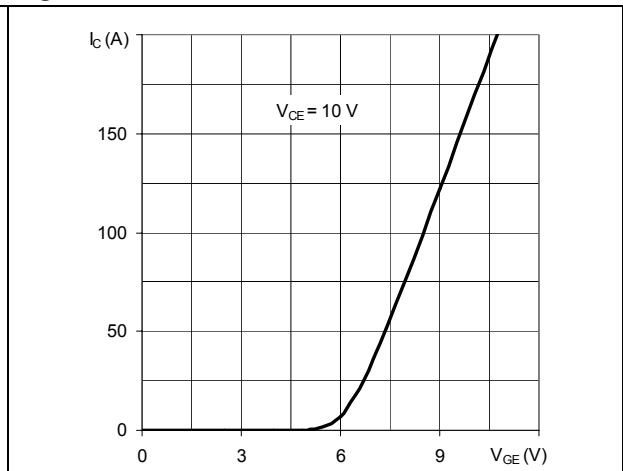


Figure 4. Normalized $V_{ce(sat)}$ vs. I_c

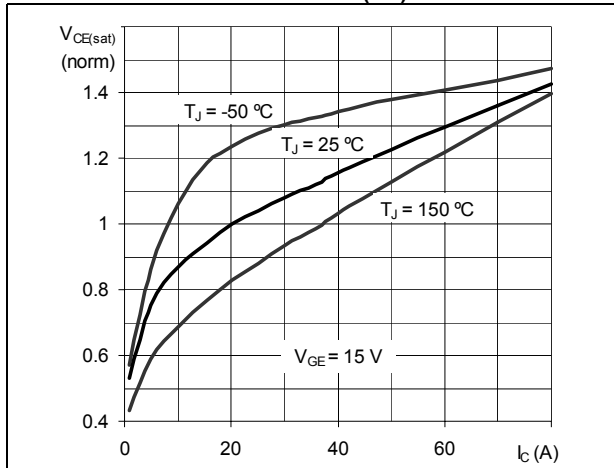


Figure 5. Normalized $V_{ce(sat)}$ vs. temperature

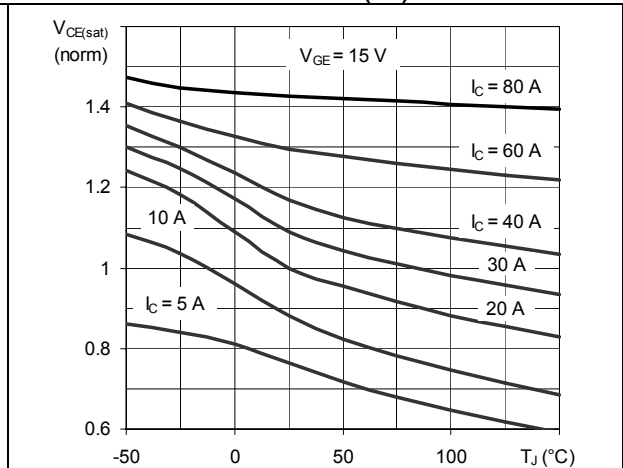


Figure 6. Normalized breakdown voltage vs. temperature

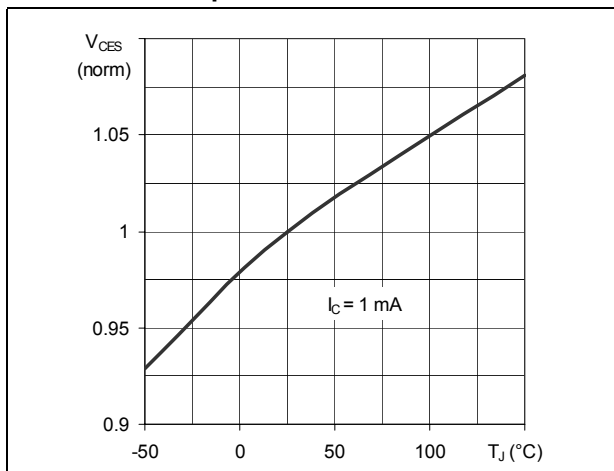


Figure 7. Normalized gate threshold voltage vs. temperature

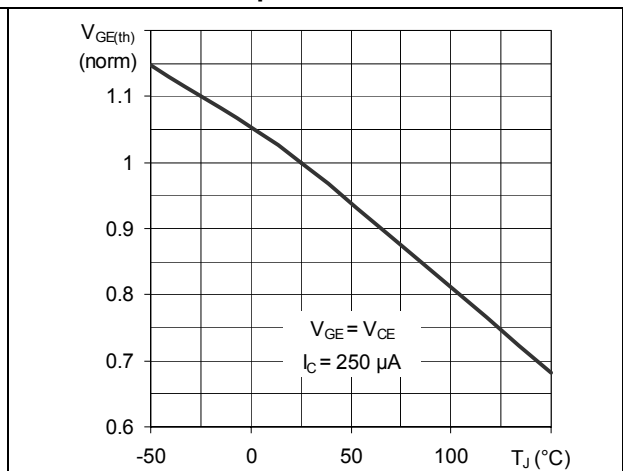


Figure 8. Gate charge vs. gate-emitter voltage

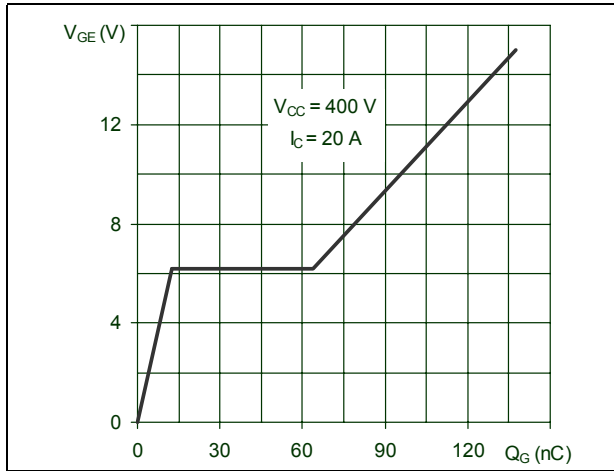


Figure 9. Capacitance variations

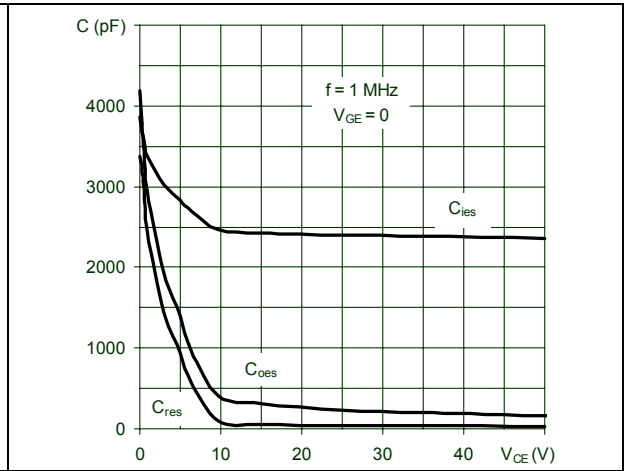


Figure 10. Switching losses vs temperature

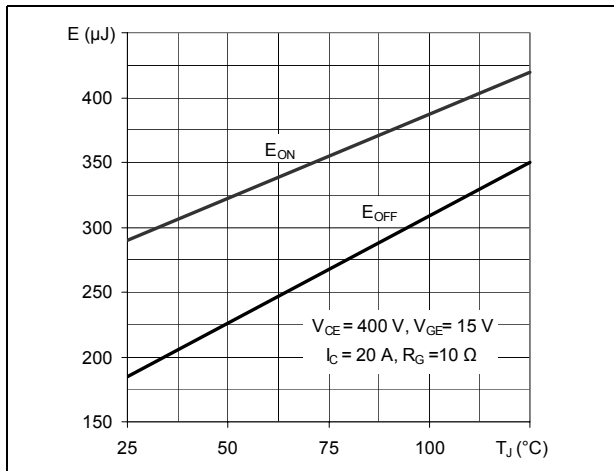


Figure 11. Switching losses vs. gate resistance

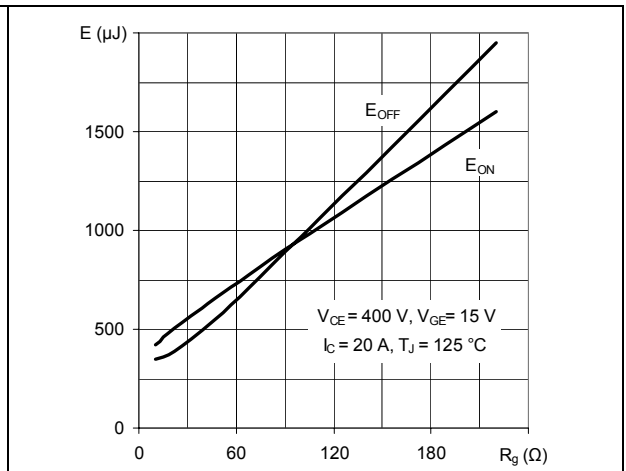


Figure 12. Switching losses vs. collector current

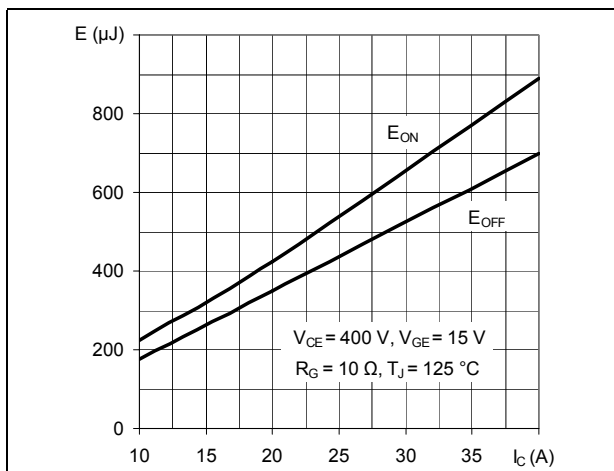


Figure 13. Turn-off SOA

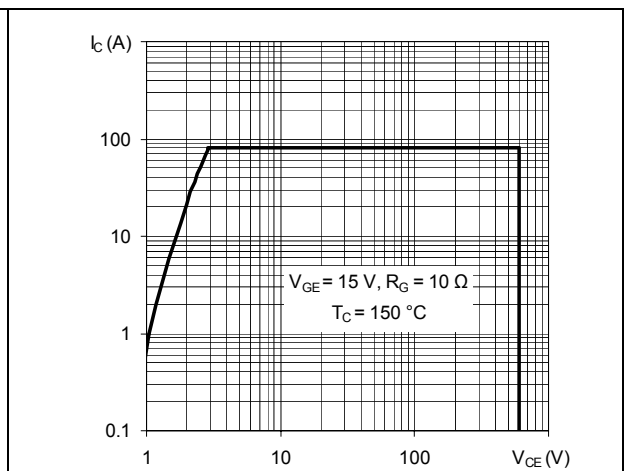


Figure 14. Thermal impedance for TO-247

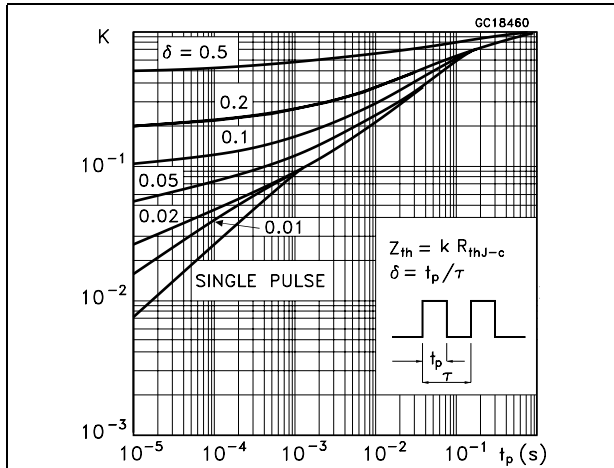
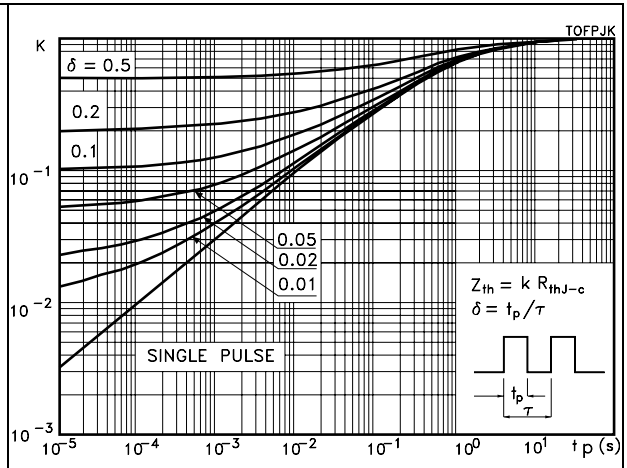


Figure 15. Thermal impedance for TO-220FP



3 Test circuits

Figure 16. Test circuit for inductive load switching

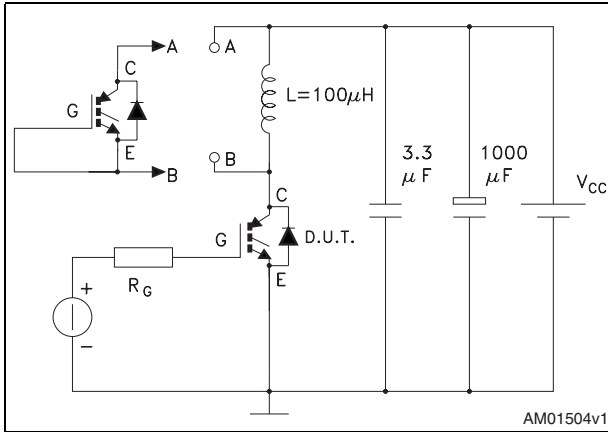


Figure 17. Gate charge test circuit

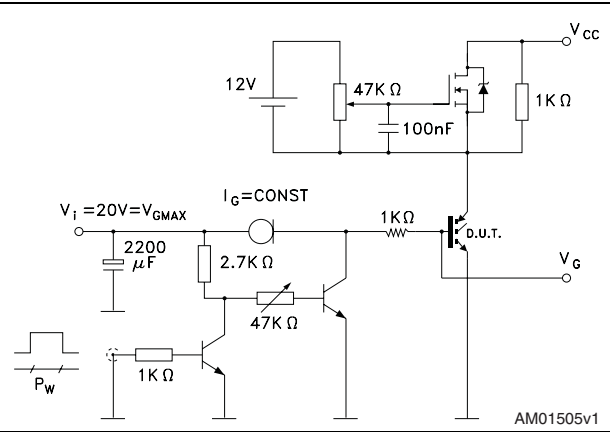
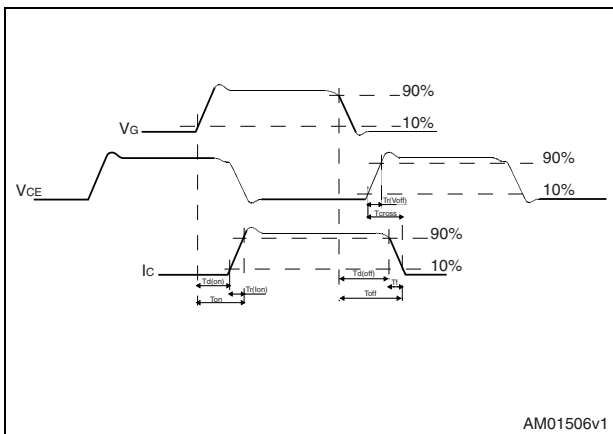


Figure 18. Switching waveform



4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 8. TO-220FP mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.4		4.6
B	2.5		2.7
D	2.5		2.75
E	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
H	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

Figure 19. TO-220FP drawing

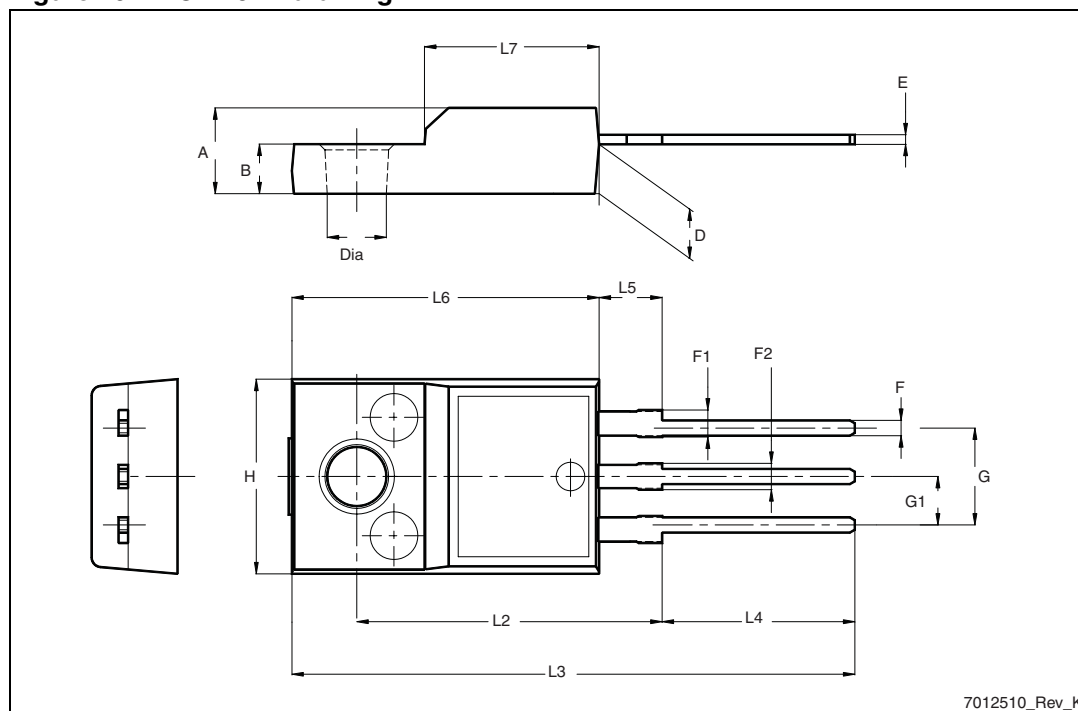
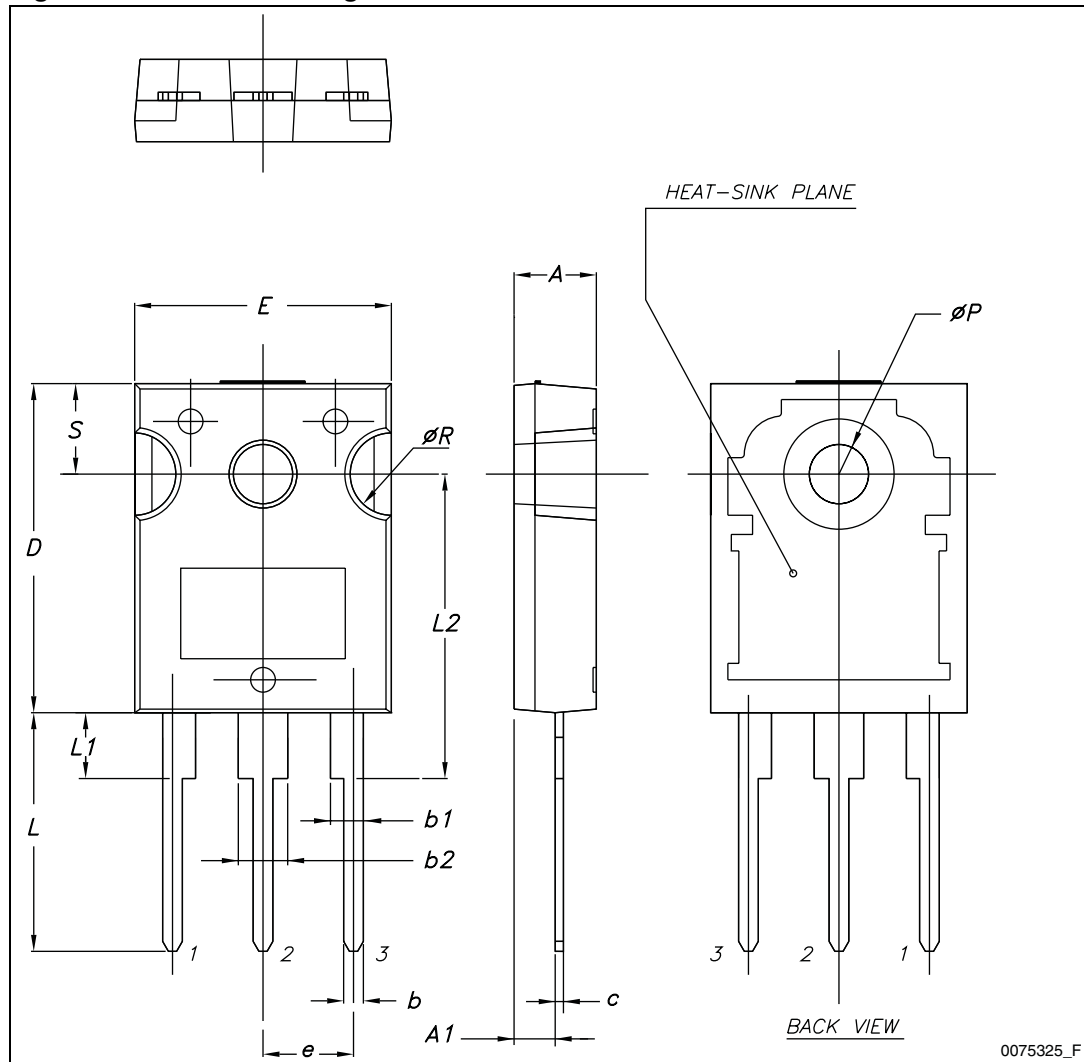


Table 9. TO-247 mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
c	0.40		0.80
D	19.85		20.15
E	15.45		15.75
e		5.45	
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S		5.50	

Figure 20. TO-247 drawing



5 Revision history

Table 10. Document revision history

Date	Revision	Changes
17-May-2010	1	Initial release.
14-Dec-2010	2	Document status promoted from preliminary data to datasheet. Inserted new order code STGF35HF60W in TO-220FP package.

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