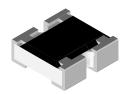
## Vishay Dale



# Thick Film Chip Resistor Attenuator, Surface Mount



#### **FEATURES**

 Single component reduces board space and component counts - replaces 3 or more components



Tolerance matching and temperature tracking superior to individuăl components

Maximum power dissipation: CZA06S; 0.040 W for CZA04S

Consult factory for extended values,

non-standard tolerances, impedance matching and other attenuation values Frequency range: DC to 3 GHz

COMPLIANT

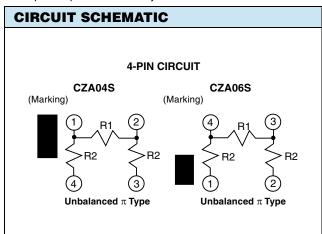
Compliant to RoHS directive 2002/95/EC

STANDARD ELECTRICAL SPECIFICATIONS					
GLOBAL MODEL	POWER RATING P <sub>70 °C</sub>	IMPEDANCE	ATTENUATION RANGE AND TOLERANCE		
GLOBAL MODEL	W	Ω	± 0.3 dB (L)	± 0.5 dB (H)	
CZA04S	0.040	50	1 dB to 5 dB	6 dB to 20 dB	
CZA06S	0.075	50/75/100/300/600	1 dB to 5 dB	6 dB to 20 dB	

#### Note

• Power rating depends on the maximum temperature at the solder point, the component placement density and the substrate material.

IMPEDANCE	50 Ω	<b>75</b> Ω	100 Ω	<b>300</b> Ω	600 $\Omega$
	1	1	1	1	1
	1.5	1.5	1.5	1.5	1.5
	2	2	2	2	2
	3	3	3	3	3
	4	4	4	4	4
	5	5	5	5	5
	6	6	6	6	6
Attenuation	10	10	10	10	10
in	11	11	11	11	11
	12	12	12	12	12
dB	13	13	13	13	13
	14	14	14	14	14
	15	15	15	15	15
	16	16	16	16	16
	17	17	17	17	17
	18	18	18	18	18
	19	19	19	19	19
	20	20	20	20	20



TECHNICAL SPECIFICATIONS					
PARAMETER	UNIT	CZA04S	CZA06S		
Rated dissipation at 70 °C	W	0.040	0.075		
VSWR		1.2 max.	1.2 max.		
Category temperature range	°C	- 55 to + 125	- 55 to + 150		
Frequency range		DC to 3 GHz	DC to 3 GHz		

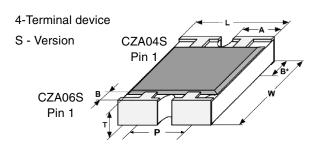
#### **GLOBAL PART NUMBER INFORMATION** New Global Part Numbering: CZA06S04015050LRT (preferred part numbering format) С Ζ Α 0 6 S 0 4 0 1 5 0 5 0 R Т MODEL **PIN COUNT** ATTENUATION **IMPEDANCE TOLERANCE PACKAGING SPECIAL** CZA04S 04 = 4 pin010 = 1.0 dB**050** = 50 $\Omega$ $H = \pm 0.5 \, dB$ EA =Lead (Pb)-free, T/R (all) (Dash number) CZA06S Up to 1 digit Blank = Standard $015 = 1.5 \, dB$ $075 = 75 \Omega$ $L = \pm 0.3 \, dB$ TD = Tin lead, T/R (04 only) **020** = 2.0 dB $\mathbf{Z} = 0 \Omega \text{ Jumper}$ **100** = 100 $\Omega$ RT = Tin lead, T/R (06 only) 150 = 15.0 dB $000 = 0 \Omega$ Jumper $\mathbf{000} = 0 \ \Omega \ \text{Jumper}$ Historical Part Number Example: CZA06S04015050LRT (will continue to be accepted) CZA **06S** 04 015 RT 050 CASE SIZE PIN COUNT TOLERANCE **PACKAGING** MODEL ATTENUATION **IMPEDANCE**

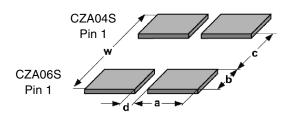
Pb containing terminations are not RoHS compliant, excemptions may apply



# Thick Film Chip Resistor Attenuator, Surface Mount

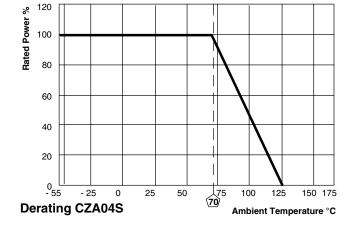
### **DIMENSIONS**

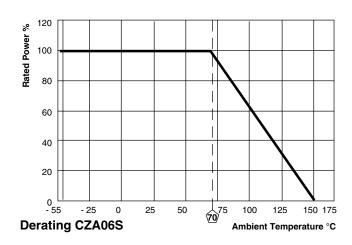




GLOBAL	DIMENSIONS in inches (millimeters)							
MODEL	L	W	Т	Α	Р	В	В*	
CZA04S	$0.039 \pm 0.004$	$0.039 \pm 0.006$	$0.014 \pm 0.004$	$0.013 \pm 0.006$	0.026	$0.006 \pm 0.004$	$0.010 \pm 0.004$	
	$(1.00 \pm 0.10)$	(1.00 ± 0.15)	(0.36 ± 0.10)	$(0.33 \pm 0.15)$	(0.65)	(0.15 ± 0.10)	(0.25 ± 0.10)	
CZA06S	$0.063 \pm 0.006$	$0.059 \pm 0.006$	$0.020 \pm 0.004$	$0.024 \pm 0.006$	0.031	$0.012 \pm 0.006$	$0.012 \pm 0.006$	
	$(1.60 \pm 0.15)$	$(1.50 \pm 0.15)$	$(0.51 \pm 0.10)$	(0.61 ± 0.15)	(0.80)	$(0.30 \pm 0.15)$	$(0.30 \pm 0.15)$	

SOLDER PAD DIMENSIONS in inches (millimeters)						
	С	w	d	а	b	
CZA04S	0.018 (0.45)	0.083 (2.10)	0.008 (0.20)	0.018 (0.45)	0.032 (0.82)	
CZA06S	0.031 (0.80)	0.122 (3.10)	0.014 (0.36)	0.025 (0.63)	0.045 (1.15)	





PERFORMANCE					
TEST	CONDITIONS OF TEST	TEST RESULTS (TYPICAL TEST LOTS)			
	CONDITIONS OF TEST	0.5 dB to 5 dB	6 dB to 20 dB		
Endurance test at 70 °C per EIA 575-3.14	1000 h at 70 °C, 1.5 h "ON", 0.5 h "OFF"	± 0.2 dB	± 0.3 dB		
Overload per EIA 575-3.6	Short time overload	± 0.2 dB	± 0.3 dB		
Thermal shock	Per EIA 575-3.5	± 0.2 dB	± 0.3 dB		
Moisture resistance	Per EIA 575-3.10	± 0.2 dB	± 0.3 dB		
Resistance to soldering heat	10 s at 260 °C solder bath temperature EIA 575 3.8	± 0.2 dB	± 0.3 dB		
High temperature exposure	Per EIA 575-3.7	± 0.2 dB	± 0.3 dB		
Low temperature operations	Per EIA-575-3.6	± 0.2 dB	± 0.3 dB		
Solderability and leaching	EIA 575-3.12	95 % coverage			

Document Number: 31061 Revision: 27-Apr-10



## **Legal Disclaimer Notice**

Vishay

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