DMG56301

Silicon NPN epitaxial planar type (Tr1) Silicon PNP epitaxial planar type (Tr2)

For digital circuits

DMG26301 in SMini5 type package

Features

- \bullet Low collector-emitter saturation voltage $V_{\mbox{CE(sat)}}$
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

Basic Part Number

DRC2114E + DRA2114E (Collector-base connection)

Packaging

Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

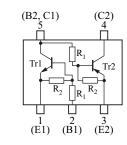
Absolute Maximum Ratings $T_a = 25^{\circ}C$

	Parameter	Symbol	Rating	Unit
	Collector-base voltage (Emitter open)	V _{CBO}	50	V
Tr1	Collector-emitter voltage (Base open)	V _{CEO}	50	V
	Collector current	I _C	100	mA
Tr2	Collector-base voltage (Emitter open)	V _{CBO}	-50	V
	Collector-emitter voltage (Base open)	V _{CEO}	-50	V
	Collector current	I _C	-100	mA
Overall	Total power dissipation	P _T	150	mW
	Junction temperature	Tj	150	°C
	Storage temperature	T _{stg}	-55 to +150	°C

Package

- Code
- SMini5-F3-B
- Pin Name
 - 1: Emitter (Tr1) 4: Collecter (Tr2)
 - 2: Base (Tr1) 5: Base (Tr2)
 - 3: Emitter (Tr2) Collecter (Tr1)
- Marking Symbol: E3

Internal Connection



	Tr1	R ₁	10	kΩ	
Resistance	111	R ₂	10	KS 2	
value	Tr2	R ₁	10	kΩ	
		R ₂	10		

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

• Tr1

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu {\rm A}, I_{\rm E} = 0$	50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 50 \text{ V}, I_E = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = 6 V, I_C = 0$			0.5	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, I_C = 5 \text{ mA}$	35			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0.5 \text{ mA}$			0.25	V
Input voltage (ON)	V _{I(on)}	$V_{\rm CE} = 0.2$ V, $I_{\rm C} = 5$ mA	2.1			V
Input voltage (OFF)	V _{I(off)}	$V_{CE} = 5 \text{ V}, I_C = 100 \mu\text{A}$			0.8	V
Input resistance	R ₁		-30%	10	+30%	kΩ
Resistance ratio	R ₁ / R ₂		0.8	1.0	1.2	

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

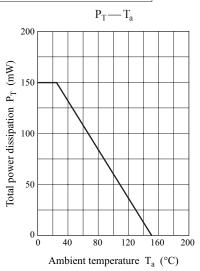
DMG56301

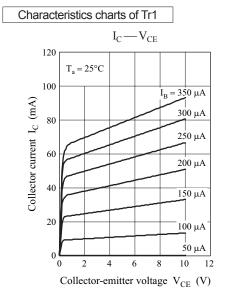
Electrical Characteristics (Continued) $T_a = 25^{\circ}C \pm 3^{\circ}C$

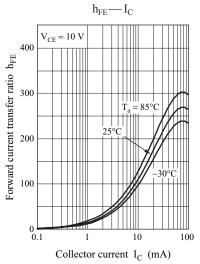
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \ \mu {\rm A}, I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -2 {\rm mA}, I_{\rm B} = 0$	-50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$			-0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{\rm CE} = -50 \text{ V}, I_{\rm B} = 0$			-0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = -6 \text{ V}, I_C = 0$			-0.5	mA
Forward current transfer ratio	h _{FE}	$V_{\rm CE} = -10$ V, $I_{\rm C} = -5$ mA	35			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -10 \text{ mA}, I_{\rm B} = -0.5 \text{ mA}$			-0.25	V
Input voltage (ON)	V _{I(on)}	$V_{CE} = -0.2 \text{ V}, I_C = -5 \text{ mA}$	-2.1			V
Input voltage (OFF)	V _{I(off)}	$V_{CE} = -5 \text{ V}, I_C = -100 \mu\text{A}$			-0.8	V
Input resistance	R ₁		-30%	10	+30%	kΩ
Resistance ratio	R ₁ / R ₂		0.8	1.0	1.2	

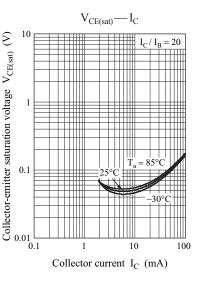
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Common characteristics chart

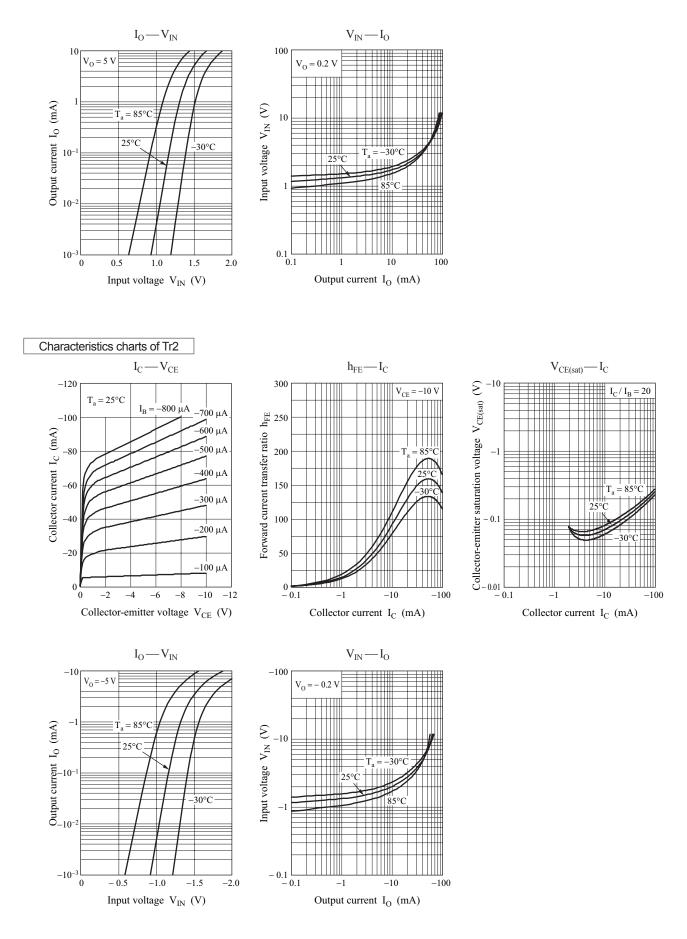






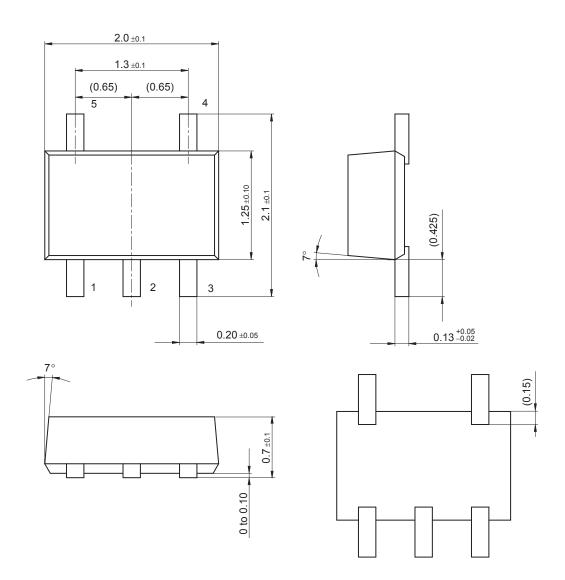


<u>Panasonic</u>



SMini5-F3-B

Unit: mm



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