# **DMC56200**

### Silicon NPN epitaxial planar type

#### For digital circuits

#### Features

- High forward current transfer ratio h<sub>FE</sub> with excellent linearity
- $\bullet$  Low collector-emitter saturation voltage  $V_{\text{CE}(\text{sat})}$
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

#### ■ Basic Part Number

Dual DRC2144T (Common base)

#### Packaging

Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	50	V	
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	50	V	
Collector current	$I_{C}$	100	mA	
Total power dissipation	$P_{T}$	150	mW	
Junction temperature	$T_j$	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	

#### ■ Package

Code

SMini5-F3-B

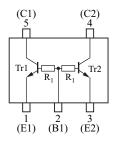
• Pin Name

1: Emitter (Tr1) 4: Collector (Tr2) 2: Base (Common) 5: Collector (Tr1)

3: Emitter (Tr2)

#### ■ Marking Symbol: P4

#### ■ Internal Connection



Resistance value	$R_1$	47	kΩ

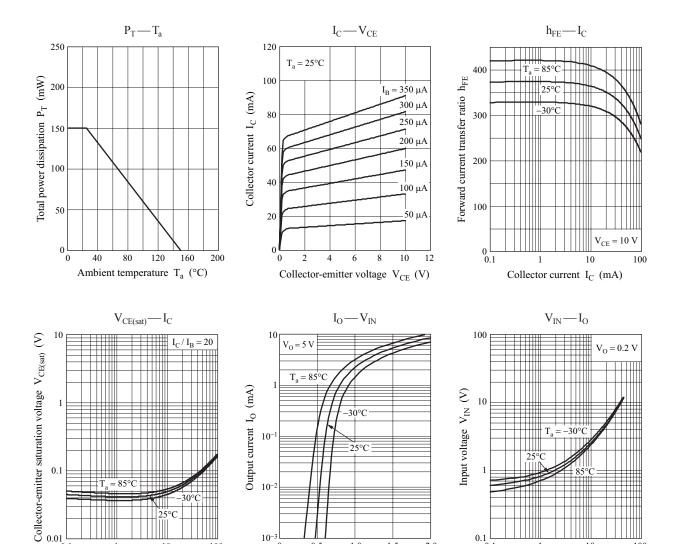
#### ■ Electrical Characteristics $T_a = 25$ °C±3°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_{\rm C} = 10  \mu A, I_{\rm E} = 0$	50			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = 50 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	$I_{EBO}$	$V_{EB} = 6 \text{ V}, I_{C} = 0$			0.01	mA
Forward current transfer ratio	$h_{\mathrm{FE}}$	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	160		460	_
h <sub>FE</sub> ratio *	h <sub>FE</sub> (Small/Large)	$V_{CE} = 10 \text{ V, } I_{C} = 5 \text{ mA}$	0.50	0.99		_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$			0.25	V
Input voltage (ON)	V <sub>I(on)</sub>	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	2.8			V
Input voltage (OFF)	V <sub>I(off)</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 100 \mu\text{A}$			0.4	V
Input resistance	$R_1$		-30%	47	+30%	kΩ

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

<sup>2. \*:</sup> Ratio between 2 elements

**Panasonic** DMC56200



1.0

Input voltage  $V_{IN}$  (V)

0.5

0.1

10

Output current IO (mA)

100

2.0

10-3 -

Ver. BED 2

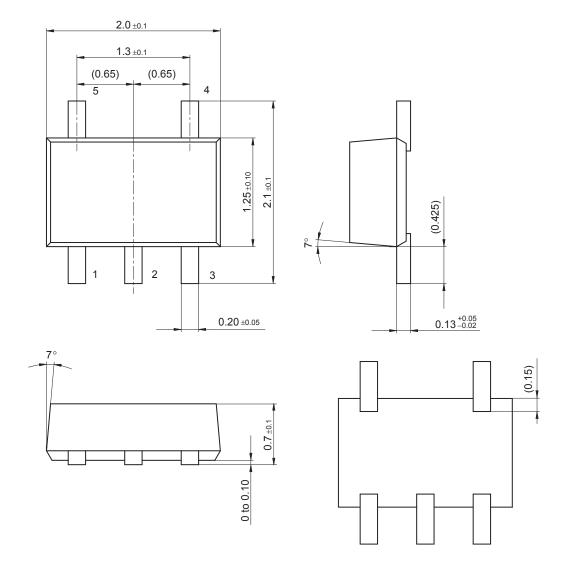
10

Collector current  $I_C$  (mA)

100

## SMini5-F3-B

Unit: mm



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