DMG26406

Silicon NPN epitaxial planar type (Tr1) Silicon PNP epitaxial planar type (Tr2)

For digital circuits

Features

- \bullet High forward current transfer ratio h_{FE} with excellent linearity
- Low collector-emitter saturation voltage $V_{CE(sat)}$
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

Basic Part Number

DRC2143T + DRA2143T (Individual)

Packaging

Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

Absolute Maximum Ratings $T_a = 25^{\circ}C$

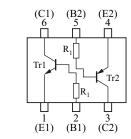
Parameter		Symbol	Rating	Unit
	Collector-base voltage (Emitter open)	V _{CBO}	50	V
Tr1	Collector-emitter voltage (Base open)	V _{CEO}	50	V
	Collector current	I _C	100	mA
Tr2	Collector-base voltage (Emitter open)	V _{CBO}	-50	V
	Collector-emitter voltage (Base open)	V _{CEO}	-50	V
	Collector current	I _C	-100	mA
Overall	Total power dissipation	P _T	300	mW
	Junction temperature	Tj	150	°C
	Storage temperature	T _{stg}	-55 to +150	°C

Package

- Code
- Mini6-G4-B
- Pin Name
 - 1: Emitter (Tr1) 4: Emitter (Tr2)
 - 2: Base (Tr1) 5: Base (Tr2)
 - 3: Collector (Tr2) 6: Collector (Tr1)

Marking Symbol: L9

Internal Connection



Resistance	Tr1	R ₁	4.7	kΩ	
value	Tr2	R ₁	4.7	kΩ	

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

• Tr1

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu {\rm A}, I_{\rm E} = 0$	50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 50 \text{ V}, I_E = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{\rm CE} = 50 \text{ V}, I_{\rm B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{\rm EB} = 6 \text{ V}, I_{\rm C} = 0$			0.01	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, I_C = 5 \text{ mA}$	160		460	
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0.5 \text{ mA}$			0.25	V
Input voltage (ON)	V _{I(on)}	$V_{CE} = 0.2 \text{ V}, I_C = 5 \text{ mA}$	1.0			V
Input voltage (OFF)	V _{I(off)}	$V_{CE} = 5 \text{ V}, I_C = 100 \mu\text{A}$			0.4	V
Input resistance	R ₁		-30%	4.7	+30%	kΩ

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

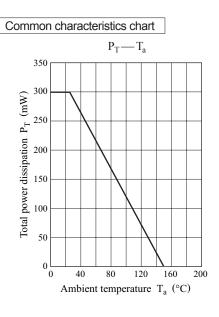
DMG26406

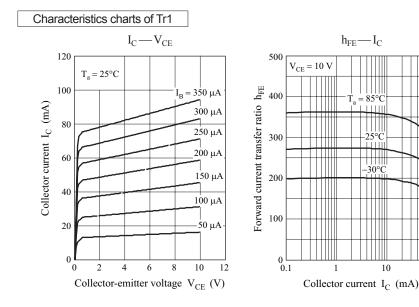
Electrical Characteristics (Continued) $T_a = 25^{\circ}C \pm 3^{\circ}C$

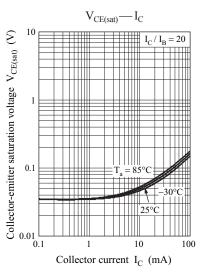
• Tr2

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \ \mu {\rm A}, I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -2 {\rm mA}, I_{\rm B} = 0$	-50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{\rm CB} = -50$ V, $I_{\rm E} = 0$			- 0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{\rm CE} = -50$ V, $I_{\rm B} = 0$			-0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{\rm EB} = -6 \text{ V}, I_{\rm C} = 0$			-0.01	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = -10 \text{ V}, I_C = -5 \text{ mA}$	160		460	
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -10 \text{ mA}, I_{\rm B} = -0.5 \text{ mA}$			-0.25	V
Input voltage (ON)	V _{I(on)}	$V_{\rm CE} = -0.2$ V, $I_{\rm C} = -5$ mA	-1.0			V
Input voltage (OFF)	V _{I(off)}	$V_{CE} = -5 \text{ V}, I_C = -100 \mu\text{A}$			-0.4	V
Input resistance	R ₁		-30%	4.7	+30%	kΩ

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

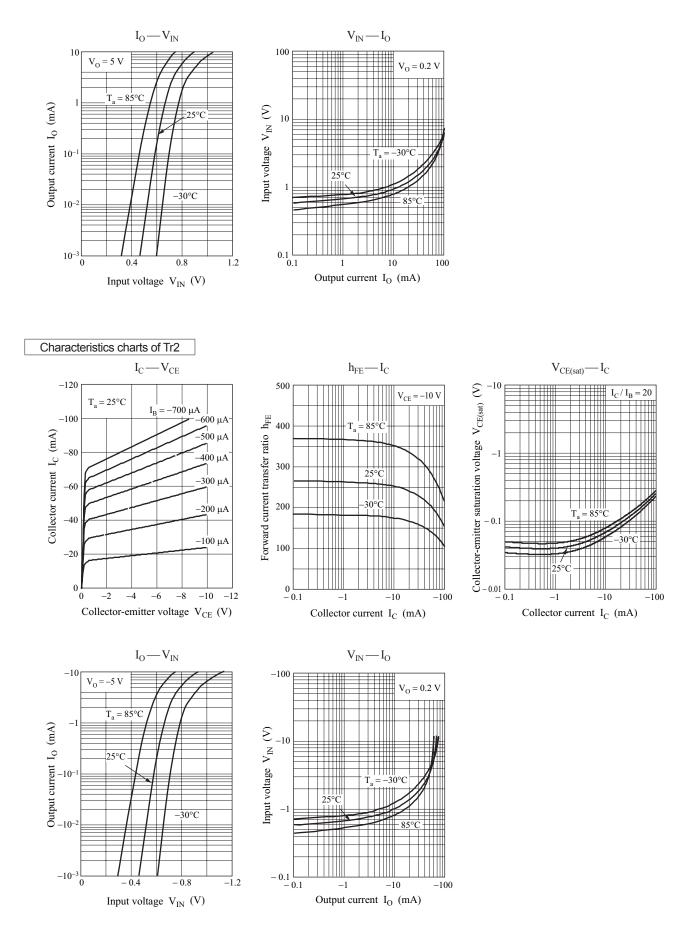






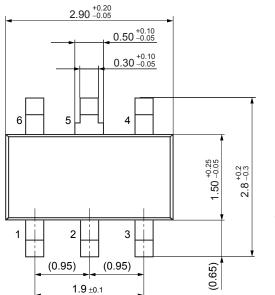
100

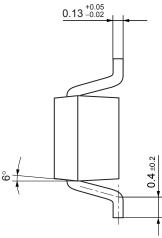
Panasonic

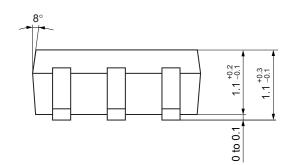


Mini6-G4-B

Unit: mm







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