LM239A-Q1 QUAD DIFFERENTIAL COMPARATOR

SCLS513B - JULY 2003 - REVISED FEBRUARY 2008

- Qualified for Automotive Applications
- ESD Protection Exceeds 1000 V Per MIL-STD-883, Method 3015; Exceeds 100 V Using Machine Model (C = 200 pF, R = 0); Exceeds 2000 V Charged Device Model
- Single Supply or Dual Supplies
- Wide Range of Supply Voltage
 ... 2 V to 36 V
- Low Supply-Current Drain Independent of Supply Voltage . . . 0.8 mA Typ
- Low Input Bias Current . . . 25 nA Typ
- Low Input Offset Current ... 5 nA Typ
- Low Input Offset Voltage ... 2 mV Typ
- Common-Mode Input Voltage Range Includes Ground
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . ±36 V
- Low Output Saturation Voltage
- Output Compatible With TTL, MOS, and CMOS

description/ordering information

This device consists of four independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible as long as the difference between the two supplies is 2 V to 36 V, and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wired-AND relationships.

ORDERING INFORMATION[†]

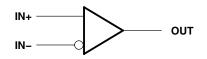
T _A	PACKAGE [‡]		ORDERABLE PART NUMBER	TOP-SIDE MARKING	
–40°C to 125°C	SOP – D	Tape and reel	LM239AQDRQ1§	LM239AQ1	

[†] For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at http://www.ti.com.

[‡] Package drawings, thermal data, and symbolization are available at http://www.ti.com/packaging.

§ This package is only available taped and reeled, with standard quantities of 2500 pieces per reel.

symbol (each comparator)





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



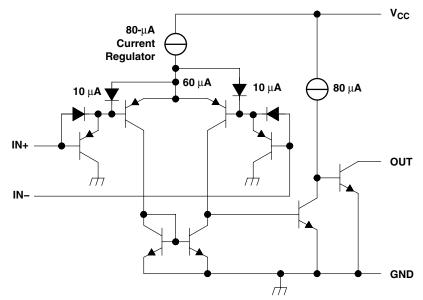
D PACKAGE (TOP VIEW)							
10UT [20UT [2IN- [2IN- [1IN- [1IN- [1 2 3 4 5 6 7	υ	14 13 12 11 10 9 8] OUT3] OUT4] GND] 4IN+] 4IN–] 3IN+] 3IN–			

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schematic (each comparator)



All current values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

$\begin{array}{llllllllllllllllllllllllllllllllllll$	±36 V 0.3 V to 36 V 36 V 20 mA Unlimited 86°C/W 260°C
Maximum operating junction temperature, T _J Storage temperature range, T _{stg}	

⁺ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages, are with respect to network ground.

2. Differential voltages are at IN+ with respect to IN-.

3. Short circuits from outputs to $V_{\mbox{CC}}$ can cause excessive heating and eventual destruction.

4. The package thermal impedance is calculated in accordance with JESD 51-7.



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PARAMETER		TEST CONDI	T _A ‡	MIN	ΤΥΡ	MAX	UNIT	
		V_{CC} = 5 V to 30 V, V_{IC}	25°C		1	2.5		
V _{IO}	Input offset voltage	V _O = 1.4 V		Full range			5.5	mV
				25°C		5	50	
IIO	Input offset current	$V_{O} = 1.4 V$		Full range			150	nA
I _{IB} Input bias current				25°C		-25	-250	nA
		V _O = 1.4 V	Full range			-400		
V _{ICR} Common-mode input-voltage range				25°C	0 to V _{CC} –1.5			v
				Full range	0 to V _{CC} –2			
A_{VD}	Large-signal differential-voltage amplification	$\label{eq:V_CC} \begin{array}{l} V_{CC} = 15 \; V, V_{O} = 1.4 \; V \; to \; 11.4 \; V, \\ R_{L} \geq 15 \; k\Omega \; to \; V_{CC} \end{array}$		25°C	50	200		V/mV
	I Bala Jawal and a summark	current V_{ID} = 1 V V_{OH} = 5 V V_{OH} = 30 V		25°C		0.1	50	nA
I _{OH}	High-level output current			Full range			1	μA
				25°C		150	400	
V _{OL} Low-level output voltage		$V_{ID} = -1 V$, $I_{OL} = 4 mA$		Full range			700	mV
I _{OL}	Low-level output current	$V_{ID} = -1 V$, $V_{OL} = 1.5 V$		25°C	6	16		mA
Icc	Supply current (four comparators)	V _O = 2.5 V,	No load	25°C		0.8	2	mA

electrical characteristics at specified free-air temperature, V_{CC} = 5 V (unless otherwise noted)

[†] All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

[‡] Full range (MIN to MAX) for LM239AQ is -40°C to 125°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER	TEST CON	MIN	TYP	MAX	UNIT	
Deenenee time	R_L connected to 5 V through 5.1 k Ω ,	100-mV input step with 5-mV overdrive	1.3 0.3			
Response time	C _L = 15 pF [§] , See Note 5	TTL-level input step			μs	

 $\ensuremath{\$}\xspace C_L$ includes probe and jig capacitance.

NOTE 5: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



www.ti.com

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
LM239AQDRG4Q1	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LM239AQDRQ1	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. **TBD:** The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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OTHER QUALIFIED VERSIONS OF LM239A-Q1 :

- Catalog: LM239A
- Enhanced Product: LM239A-EP

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Enhanced Product Supports Defense, Aerospace and Medical Applications

D (R-PDSO-G14)

PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AB.



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