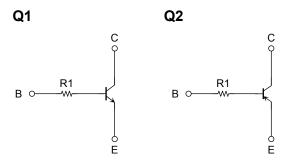
TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

# **RN4993FS**

Switching, Inverter Circuit, Interface Circuit and **Driver Circuit Applications** 

- Two devices are incorporated into a fine pitch small mold (6-pin) ٠ package.
- · Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

#### **Equivalent Circuit and Bias Resistor Values**



R1: 47kΩ

(Q1, Q2 common)

Equivalent Marking

Circuit

τo2

3

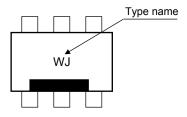
2

Q1





Weight: 0.001g (typ.)



1.0±0.05 0.8±0.05 0.1±0.05 0.1±0.05 0.15±0.05 6 35 5 1±0.05 0 1.EMIITTER1 (E1) 2.BASE1 (B1) (C2)

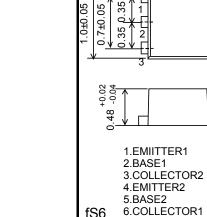
(E2)

(B2)

(C1)

\_

2-1F1D



fS6

JEDEC

JEITA

TOSHIBA

Unit: mm

#### Absolute Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	20	V
Collector-emitter voltage	V <sub>CEO</sub>	20	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	Ι <sub>C</sub>	50	mA

#### Absolute Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-20	V
Collector-emitter voltage	V <sub>CEO</sub>	-20	V
Emitter-base voltage	V <sub>EBO</sub>	-5	V
Collector current	Ι <sub>C</sub>	-50	mA

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> (Note 1)	50	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

### Electrical Characteristics (Ta = 25°C) (Q1)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB}=20~V,~I_{E}=0$	_	_	100	nA
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=5~V,~I_C=0$	_	_	100	nA
DC current gain	h <sub>FE</sub>	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 1 \text{ mA}$	300	_	—	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_{C} = 5 \text{ mA}, I_{B} = 0.25 \text{ mA}$	_	_	0.15	V
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$	_	1.2		pF

#### Electrical Characteristics (Ta = 25°C) (Q2)

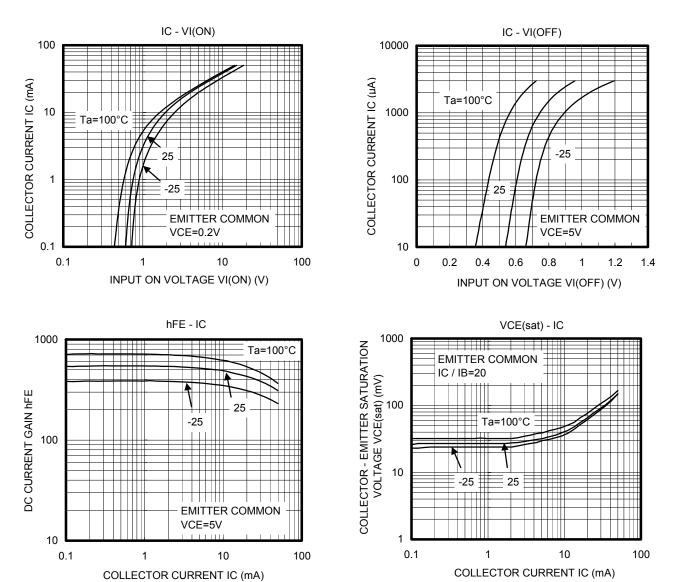
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB}=-20~V,~I_{E}=0$	_	_	-100	nA
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=-5~V,~I_C=0$	_	_	-100	nA
DC current gain	h <sub>FE</sub>	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -1 \text{ mA}$	300	_	—	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	_	-0.15	V
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		1.2		pF

### Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1		37.6	47	56.4	kΩ

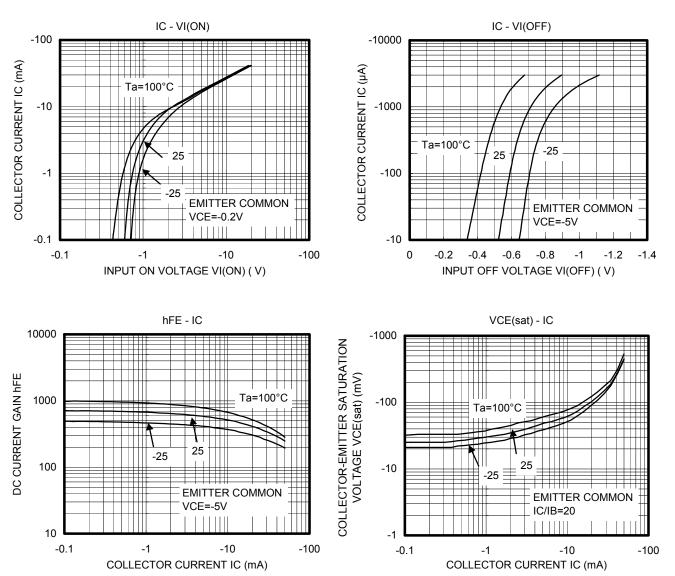
# <u>TOSHIBA</u>

Q1



## <u>TOSHIBA</u>

Q2



#### **Handling Precaution**

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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