

100mA / 50V Digital transistors

(with built-in resistors)

DTC143XM / DTC143XE / DTC143XUA / DTC143XKA

Applications

Inverter, Interface, Driver

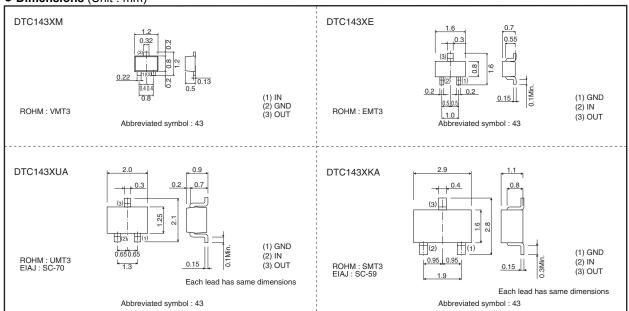
Features

- 1)Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see the equivalent circuit).
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3)Only the on/off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

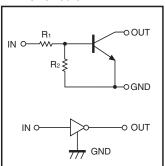
• Dimensions (Unit : mm)



Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3
	Packaging type	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146
Part No.	Basic ordering unit (pieces)	8000	3000	3000	3000
DTC143XM		0	-		
DTC143XE		-	O -		_
DTC143XUA		-	_	0	-
DTC143XKA		-			0

• Inner circuit



R₁=4.7k Ω , R₂=10k Ω

• Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Lin	Unit	
Farameter		DTC143XM DTC143XE	DTC143XUA DTC143XKA	Onit
Supply voltage	Vcc	50		V
Input voltage	VIN	−7 to	V	
Outrout accurant	lo	10	mA	
Output current	IC(Max.)	10		
Power dissipation	Po	150	200	mW
Junction temperature	Tj	15	°C	
Storage temperature	Tstg	–55 to	°C	

• Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
lanut valta sa	VI(off)	-	-	0.3	V	Vcc=5V, Io=100μA
Input voltage	VI(on)	2.5	-	_		Vo=0.3V, Io=20mA
Output voltage	V _{O(on)}	-	0.1	0.3	V	lo/l≔10mA/0.5mA
Input current	lı .	-	-	1.8	mA	Vi=5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, V⊫0V
DC current gain	Gı	30	-	-	-	Vo=5V, Io=10mA
Input resistance	R ₁	3.29	4.7	6.11	kΩ	_
Resistance ratio	R2/R1	1.7	2.1	2.6	-	_
Transition frequency	f⊤ *	_	250	_	MHz	VcE=10V, IE=-5mA, f=100MHz

^{*} Characteristics of built-in transistor

• Electrical characteristic curves

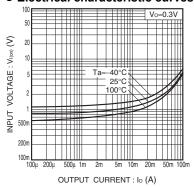


Fig.1 Input voltage vs. output current (ON characteristics)

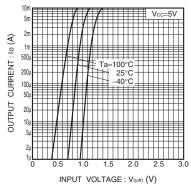


Fig.2 Output current vs. input voltage (OFF characteristics)

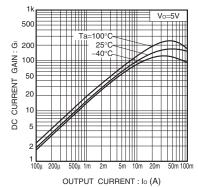


Fig.3 DC current gain vs. output current

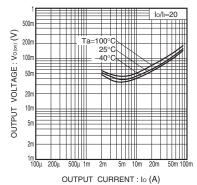


Fig.4 Output voltage vs. output current

Notes

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