LM20242

Application Note 1694 LM20242 Evaluation Board



Literature Number: SNVA280A

LM20242 Evaluation Board

National Semiconductor Application Note 1694 Steven Schulte October 30, 2007

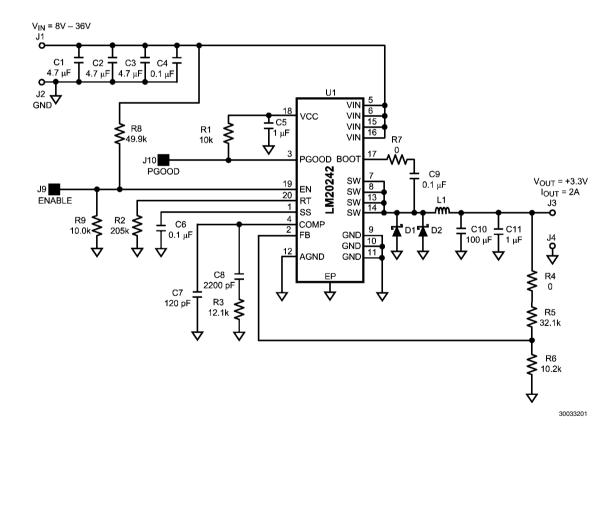


Introduction

The LM20242 synchronous rectifier Buck evaluation board is designed to provide the design engineer with a fully functional Buck power converter based on Current Mode Control to evaluate the LM20242 switching regulator IC. The evaluation board provides a 3.3V output with 2A current capability. The input voltage ranges from 8V to 36V. The design operates at 300 kHz, a good compromise between conversion efficiency and solution size. The printed circuit board consists of 2 layers of 2 ounce copper on FR4 material with a thickness of 0.062 inches. This application note contains the evaluation board schematic, Bill-of-Materials (BOM) and a quick setup procedure. Refer to the LM20242 datasheet for complete circuit design information.

Evaluation Board Schematic

The performance of the synchronous rectifier buck evaluation board is as follows: Input Range: 8V to 36V Output Voltage: $3.3V, \pm 2\%$ Output Current: 0 to 2A Frequency of Operation: 300 kHz Board Size: $2.15 \times 2.0 \times 0.6$ inches Load Regulation: 0.1%Line Regulation: 0.1%Over Current Limiting



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Powering and Loading Considerations

Read this entire page prior to attempting to power the evaluation board.

QUICK SETUP PROCEDURE

Step 1: Set the input source current limit to 1A. Turn off the input source. Connect the positive output of the input source to J1 and the negative output to J2.

Step 2: Connect the load, with 2A capability, to J3 for the positive connection and J4 for the negative connection.

Step 3: The ENABLE pin, J9, should be left open for normal operation.

Step 4: Set the input source voltage to 12V and the load to 0.1A. The load voltage should be in regulation with a nominal 3.3V output.

Step 5: Slowly increase the load while monitoring the load voltage at J3 and J4. It should remain in regulation with a nominal 3.3V output as the load is increased up to 2 Amp.

Step 6: Slowly sweep the input source voltage from 8V to 36V. The load voltage should remain in regulation with a nominal 3.3V output.

Step 7: Temporally short the ENABLE pin (J9) to GND (J2) to check the shutdown function.

Step 8: Increase the load beyond the normal range to check current limiting while the input source is set to 12V. The output current should limit at approximately 3.8A. The input source current limit should be increased for this step. Fan cooling is critical during this step.

AIR FLOW

Prolonged operation at full power and high ambient temperature will cause the thermal shutdown circuit within the regulator IC to activate. A fan with a minimum of 200 LFM should always be provided.

POWERING UP

Using the ENABLE pin (J9) provided will allow powering up the input source with the current level set low. It is suggested that the load power be kept low during the first power up. Set the current limit of the input source to provide about 1.5 times the anticipated wattage of the load. As you remove the connection from the ENABLE pin to GND (J2), immediately check for 3.3 volts at the output.

A quick efficiency check is the best way to confirm that everything is operating properly. If something is amiss you can be reasonably sure that it will affect the efficiency adversely. Few parameters can be incorrect in a switching power supply without creating losses and potentially damaging heat.

OVER CURRENT PROTECTION

The evaluation board is configured with cycle-by-cycle overcurrent protection. This function is completely contained in the LM20242. The peak current is limited to approximately 3.8A. The thermal stress on various circuit components is quite severe while in an overloaded condition, therefore limit the duration of the overload and provide sufficient cooling (airflow).

Performance Characteristics

EFFICIENCY PLOTS

Figure 1 shows the conversion efficiency versus output current for several input voltage conditions.

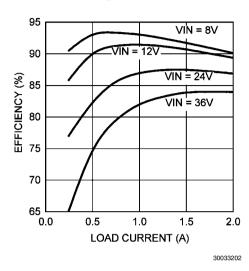
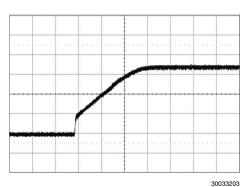


FIGURE 1.

TURN-ON WAVEFORM

When applying power to the LM20242 evaluation board a soft-start sequence occurs. Figure 2 shows the output voltage during a typical start-up sequence.

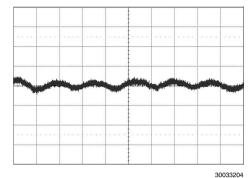


Conditions: Input Voltage = 12VDC, Output Current = 2A Trace 1: Output Voltage Volts/div = 1V Horizontal Resolution = 5ms/div

FIGURE 2.

OUTPUT RIPPLE WAVEFORM

Figure 3 shows the output voltage ripple. This measurement was taken with the scope probe tip placed on the J3 load terminal and the scope probe ground "barrel" pushed against the J4 load terminal. The scope bandwidth is set to 20 MHz.



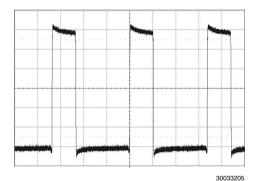
Conditions: Input Voltage = 12VDC, Output Current = 2A, Bandwidth Limit = 20 MHZ

Trace 1: Output Ripple Voltage Volts/div = 20 mV Horizontal Resolution = 2 μ s/div

FIGURE 3.

PRIMARY SWITCHNODE WAVEFORM

FIGURE 4 shows the typical SW pin voltage during continuous conduction mode (CCM).



Conditions: Input Voltage = 12VDC, Output Current = 2A, Bandwidth Limit = 20 MHZ Trace 1: LM20242 SW Pin Volts/div = 2V Horizontal Resolution = 1 µs/div

FIGURE 4.

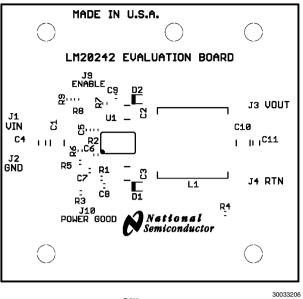
Layout and Bill of Materials

The Bill of Materials is shown below, including the manufacturer and part number.

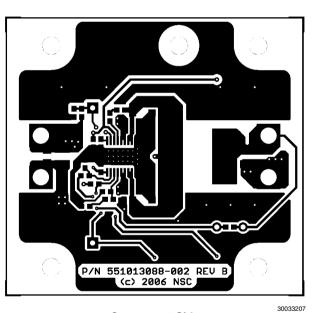
TABLE 1.

DESIGNATOR	QTY	PART NUMBER	DESCRIPTION	VALUE	
C1-3	3	GRM32ER71H475KA88L	CAPACITOR, 1210 X7R CER, MURATA	4.7µ, 50V	
C4	1	C2012X7R2A104K	CAPACITOR, 0805 X7R CER, TDK	0.1µ, 100V	
C5	1	GRM188R71A105KA61D	CAPACITOR, 0603 X7R CER, MURATA	1µ, 10V	
C6,9	2	C1608X7R1H104K	CAPACITOR, 0603 X7R CER, TDK	0.1µ, 50V	
C7	1	C1608C0G1H121J	CAPACITOR, 0603 COG CER, TDK	120p, 50V	
C8	1	C1608C0G1H222J	CAPACITOR, 0603 COG CER, TDK	2200p, 50V	
C10	1	C1210C107M9PAC	CAPACITOR, 1210 X5R CER, KEMET	100µ, 6.3V	
C11	1	GRM21BR71H105KA12L	CAPACITOR, 0805 X7R CER, MURATA	1µ, 50V	
D1,2	2	MBR0540	DIODE, SCHOTTKY, SOD123, FAIRCHILD	500 mA, 40V	
L1	1	MSS1278-153ML	INDUCTOR, COILCRAFT 12 mm x 12 mm	15 µH, 3.5A	
R1,9	2	CRCW06031002F	RESISTOR, 0603, VISHAY	10k	
R2	1	CRCW06032053F	RESISTOR, 0603, VISHAY	205k	
R3	1	CRCW06031212F	RESISTOR, 0603, VISHAY	12.1k	
R4,7	2	CRCW06030000Z0EA	RESISTOR, 0603, VISHAY	0	
R5	1	CRCW06033212F	RESISTOR, 0603, VISHAY	32.1k	
R6	1	CRCW06031022F	RESISTOR, 0603, VISHAY	10.2k	
R8	1	CRCW06034992F	RESISTOR, 0603, VISHAY	49.9k	
U1	1	LM20242MH	SWITCHING REGULATOR, NATIONAL SEMI		
J1,2,3,4	4	1503-2	0.112" TURRET TERMINAL, KEYSTONE	TERMINAL, TURRET	
J9,10	2	5002	TERMINAL, SINGLE PIN, KEYSTONE	TESTPOINT, LOOP	
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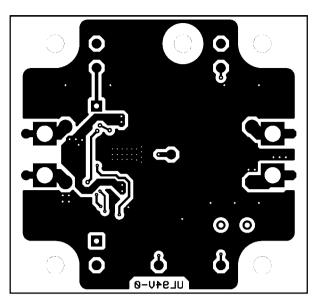
PCB Layout



Silkscreen



Component Side



Solder Side

30033208

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Notes

Pr	oducts	Design Support		
Amplifiers	www.national.com/amplifiers	WEBENCH	www.national.com/webench	
Audio	www.national.com/audio	Analog University	www.national.com/AU	
Clock Conditioners	www.national.com/timing	App Notes	www.national.com/appnotes	
Data Converters	www.national.com/adc	Distributors	www.national.com/contacts	
Displays	www.national.com/displays	Green Compliance	www.national.com/quality/green	
Ethernet	www.national.com/ethernet	Packaging	www.national.com/packaging	
Interface	www.national.com/interface	Quality and Reliability	www.national.com/quality	
LVDS	www.national.com/lvds	Reference Designs	www.national.com/refdesigns	
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