



MSP430-CCRF development board Users Manual



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INTRODUCTION:

MSP430-CCRF is development board with CC430F5137IRGZ microcontroller by Texas Instruments. This ultra-low-power microcontroller has integrated CC1101 RF transceiver. The board has also UEXT, which can be modified as device or host. MSP430-CCRF can communicate with other boards with integrated CC1101 RF transceiver via radio connection.

BOARD FEATURES:

- Microcontroller: CC430F5137IRGZ
- JTAG
- UEXT
- PWR jack for 2 x 1.5V AA batteries
- on-board voltage regulator
- On-bard pcb antenna
- Supported radio frequencies bands 868/915 MHz software selectable. By default – 868 MHz.
- Maximal permissible distance between two MSP430-CCRF boards depends on RF chip's output power, data transfer rate and the selected radio frequency band. For more information, see the table below.
- user button
- status led
- RTC crystal
- extension pin holes for most of the microcontroller pins
- FR-4, 1.00 mm, soldermask, component print
- Dimensions: 78.49 x 39.12mm (3.09 x 1.54")

Output Power	Data Rate	Measured Distance
10dBm	1.2kBaud	155m
10dBm	38.4kBaud	110m
10dBm	250kBaud	85m
10dBm	1.2kBaud	140m
10dBm	38.4kBaud	100m
10dBm	250kBaud	60m
	10dBm 10dBm 10dBm 10dBm 10dBm	10dBm 1.2kBaud 10dBm 38.4kBaud 10dBm 250kBaud 10dBm 1.2kBaud 10dBm 38.4kBaud 10dBm 38.4kBaud

This measurements were taken via using "RF Connection_toggle LED" demo software loaded at two alike MSP430-CCRF boards!

ELECTROSTATIC WARNING:

The **MSP430-CCRF** board is shipped in protective anti-static packaging. The board must not be subject to high electrostatic potentials. General practice for working with static sensitive devices should be applied when working with this board.

BOARD USE REQUIREMENTS:

Cables: The cable you will need depends on the programmer/debugger you use. If you use MSP-JTAG-TINY, MSP-JTAG-TINY-V2, or MSP-JTAG-ISO, you will need USB A-B cable. If you use MSP-JTAG, you will need LPT cable.

Hardware: Programmer/Debugger <u>MSP-JTAG-TINY</u>, MSP-JTAG-TINY-V2, <u>MSP-JTAG-ISO</u>, <u>MSP-JTAG</u>, <u>MSP-JTAG-RF</u>, or other compatible programming/debugging tool.

You also can use other Olimex's MSP-CCRF or MSP-CCRF-LCD board for radio communication.

PROCESSOR FEATURES:

MSP430-CCRF board use microcontroller CC430F5137IRGZFBD100 with these features:

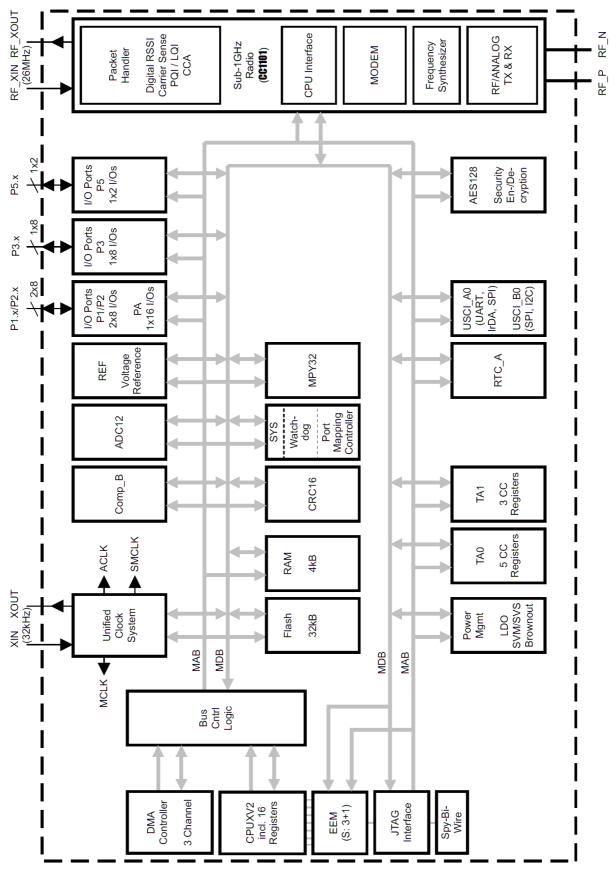
- True System-on-Chip (SoC) for Low-Power Wireless Communication Applications
- Wide Supply Voltage Range: 1.8 V to 3.6 V
- Ultra-Low Power Consumption:
 - CPU Active Mode (AM): 160 µA/MHz
 - Standby Mode (LPM3 RTC Mode):2.0 µA
 - Off Mode (LPM4 RAM Retention): 1.0 µA
 - Radio in RX: 15 mA, 250 kbps, 915 MHz
- MSP430[™] System and Peripherals
 - 16-Bit RISC Architecture, Extended Memory, up to 20-MHz System Clock
 - Wake-Up From Standby Mode in Less Than 6 µs
 - Flexible Power Management System with SVS and Brownout
 - Unified Clock System with FLL
 - 16-Bit Timer TA0, Timer_A with Five Capture/Compare Registers
 - 16-Bit Timer TA1, Timer_A with Three Capture/Compare Registers
 - Hardware Real-Time Clock
 - Two Universal Serial Communication Interfaces
 - USCI_A0 supporting UART, IrDA, SPI
 - USCI_B0 supporting I²C, SPI
 - 12-Bit A/D Converter With Internal Reference, Sample-and-Hold, and Autoscan Features
 - Comparator
 - 128-bit AES Security Encryption/Decryption Coprocessor
 - 32-Bit Hardware Multiplier
 - Three-Channel Internal DMA

- Serial Onboard Programming, No External Programming Voltage Needed
- Embedded Emulation Module (EEM)
- High-Performance Sub-1-GHz RF Transceiver Core
 - Wide Supply Voltage Range: 2.0 V to 3.6 V
 - Frequency Bands: 300 MHz to 348 MHz, 389 MHz to 464 MHz, and 779 MHz to 928 MHz.
 - Programmable Data Rate From 0.6 kBaud to 500 kBaud
 - High Sensitivity (-117 dBm at 0.6 kBaud, -111 dBm at 1.2 kBaud, 315 MHz, 1% Packet Error Rate)
 - Excellent Receiver Selectivity and Blocking Performance
 - Programmable Output Power Up to +12 dBm for All Supported Frequencies
 - 2-FSK, 2-GFSK, and MSK Supported as well as OOK and Flexible ASK Shaping
 - Flexible Support for Packet-Oriented Systems: On-Chip Support for Sync Word Detection, Address Check, Flexible Packet Length, and Automatic CRC Handling
 - Support for Automatic Clear Channel Assessment (CCA) Before Transmitting (for Listen-Before-Talk Systems)
 - Digital RSSI Output
 - Suited for Systems Targeting Compliance With EN 300 220 (Europe) and FCC CFR Part 15 (US)
 - Suited for Systems Targeting Compliance With Wireless M-Bus Standard EN 13757-4:2005
 - Support for Asynchronous and Synchronous Serial Receive/Transmit Mode for Backward Compatibility With Existing Radio Communication Protocols

			USCI				
Program (KB)	SRAM (KB)	Timer_A (1)	Channel A: UART/LIN /IrDA/SPI	Channel B: SPI/ I ² C	ADC12_A	Comp_B	I/O
32	4	5, 3	1	1	6 ext/ 4 int ch.	6 ch.	30

(1) 5, 3 - represents two instantiations of Timer_A, the first instantiation having 5 and the second instantiation having 3 capture compare registers and PWM output generators, respectively.





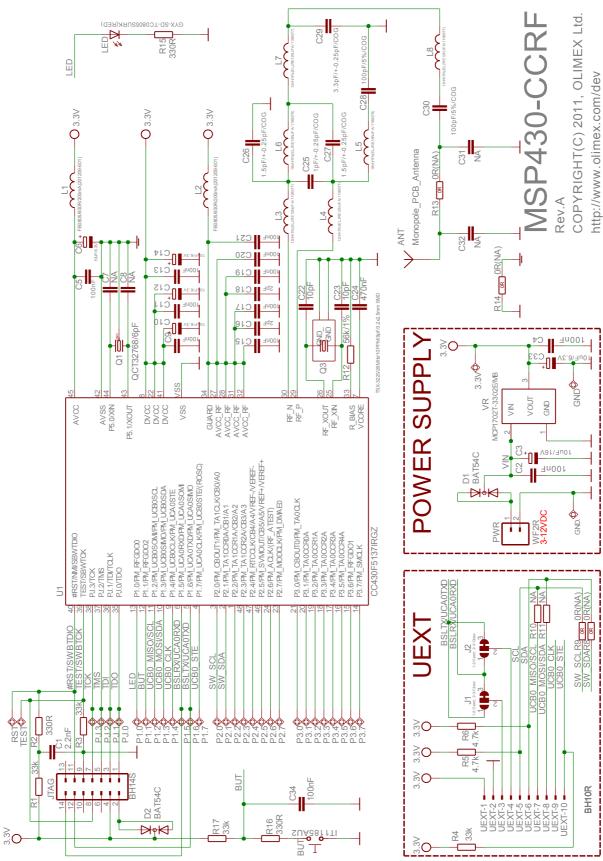
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MEMORY ORGANIZATION:

		CC430F5137 ⁽¹⁾
Main Memory (flash)	Total Size	32kB
Main: Interrupt vector		00FFFFh to 00FF80h
Main: code memory	Bank 0	32kB 00FFFFh to 008000h
RAM	Total Size	4kB
	Sect 1	2kB 002BFFh to 002400h
	Sect 0	2kB 0023FFh to 001C00h
Device Descriptor		128 B 001AFFh to 001A80h
		128 B 001A7Fh to 001A00h
	Info A	128 B 0019FFh to 001980h
Information	Info B	128 B 00197Fh to 001900h
memory (flash)	Info C	128 B 0018FFh to 001880h
	Info D	128 B 00187Fh to 001800h
	BSL 3	512 B 0017FFh to 001600h
Bootstrap loader (BSL) memory (flash)	BSL 2	512 B 0015FFh to 001400h
	BSL 1	512 B 0013FFh to 001200h
	BSL 0	512 B 0011FFh to 001000h
Peripherals		4 KB 000FFFh to 0h

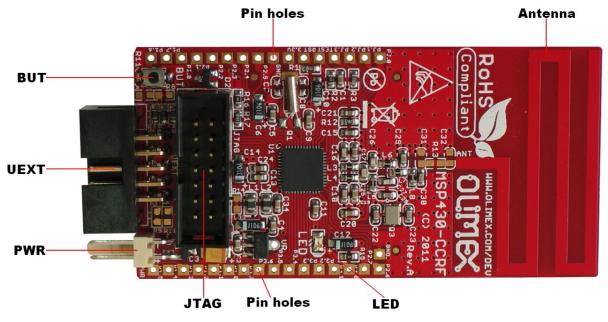
(1) All memory regions not specified here are vacant memory, and any access to them causes a Vacant Memory Interrupt.

SCHEMATIC:



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BOARD LAYOUT:



POWER SUPPLY CIRCUIT:

MSP430-CCRF can take power from:

-External power supply via PWR connector 3 - 12 VDC

-JTAG

The programmed board power consumption is about 25 mA with all peripherals enabled.

RESET CIRCUIT:

MSP430-CCRF reset circuit includes R2 (330Ω), R1 (33k), C1 (2.2nF), JTAG pin 11 and CC430F5137IRGZ pin 40 (#RST/SWBTDIO).

CLOCK CIRCUIT:

Quartz crystal Q1 32.768 kHz is connected to CC430F5137IRGZ pin 44 (P5.0/XIN) and pin 43 (P5.1/XOUT).

Quartz crystal ${\bf Q3}$ 26 MHz is connected to CC430F5137IRGZ pin 25 (RF_XIN) and pin 26 (RF_XOUT).

JUMPER DESCRIPTION:



This jumper, when is in position 1-2 – UEXT pin 3 (signal TX) is connected to CC430F5137IRGZ pin 5 (signal BSLTX/UCA0TXD); when the jumper is in position 2-3- UEXT pin 3 (signal TX) is connected to CC430F5137IRGZ pin 6 (signal BSLRX/UCA0RXD)

Default state is shorted in position 2-3.



This jumper, when is in position 1-2 – UEXT pin 4 (signal RX) is connected to CC430F5137IRGZ pin 6 (signal BSLRX/UCA0RXD); when the jumper is in position 2-3 - UEXT pin 4 (signal RX) is connected to CC430F5137IRGZ pin 5 (signal BSLTX/UCA0TXD)

Default state is shorted in position 2-3.

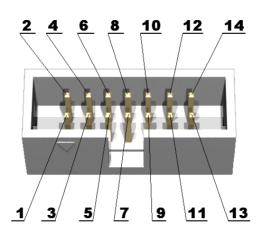
When jumpers J1 and J2 are shorted in position 1-2 – the board is configured as host and can be used as host for some of our module boards. When these jumpers are shorted in position 2-3 – the board is configured as device and can be used as module board.

INPUT/OUTPUT:

Status Led with name **LED (red)** connected to CC430F5137IRGZ pin 13 (P1.0/PM_RFGDO0).

User button with name **BUT** connected to **CC430F5137IRGZ** pin 12 (P1.1/PM_RFGDO2).

EXTERNAL CONNECTORS DESCRIPTION: JTAG:



Pin #	Signal Name	Pin #	Signal Name
1	TDO	2	JTAG power supply
3	TDI	4	VCC
5	TMS	6	Not Connected
7	TCK	8	TEST/SWBTCK
9	GND	10	Not Connected
11	#RST/SWBTDIO	12	BSLTX/UCA0TXD
13	Not Connected	14	BSLRX/UCA0RXD

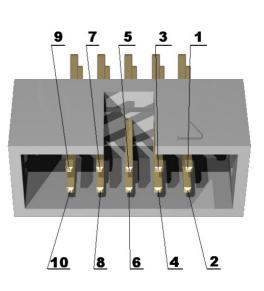
<u>PWR:</u>

Pin #	Signal Name
1	VIN
2	GND



<u>UEXT:</u>

Pin #	Signal Name
1	VCC
2	GND
3	ТХ
4	RX
5	SCL
6	SDA
7	UCB0_MISO/SC L
8	UCB0_MOSI/SD A
9	UCB0_CLK
10	UCB0_STE

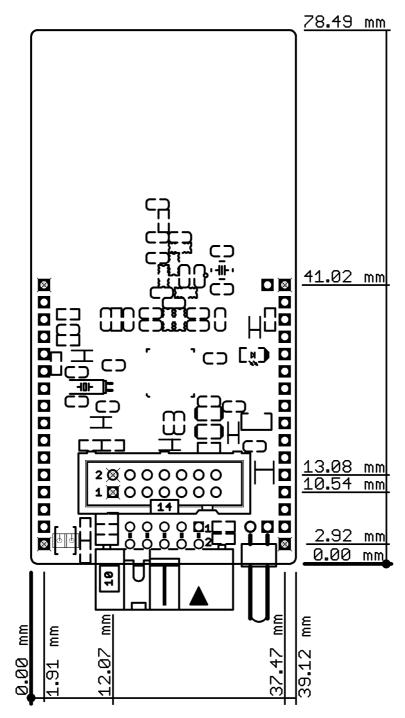


<u>Pin holes:</u>

Pin #	Signal Name	Pin #	Signal Name
PJ.0	TDO	P2.6	P2.6
PJ.1	TDI	P2.7	P2.7
PJ.2	TMS	P3.0	P3.0
PJ.3	ТСК	P3.1	P3.1
TEST	TEST/SWBTCK	P3.2	P3.2
RST	#RST/SWBTDIO	P3.3	P3.3
3.3V	VCC	P3.4	P3.4
GND	GND	P3.5	P3.5
P2.5	P2.5	P3.6	P3.6
P2.4	P2.4	P3.7	P3.7
P2.3	P2.3	P1.0	LED
P2.2	P2.2	P1.1	BUT
P2.1	SW_SDA	P1.2	UCB0_MISO/SCL
P2.0	SW_SCL	P1.3	UCB0_MOSI/SDA
P1.7	UCB0_STE	P1.4	UCB0_CLK
P1.6	BSLTX/UCA0TX D	P1.5	BSLRX/UCA0RXD

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MECHANICAL DIMENSIONS:



AVAILABLE DEMO SOFTWARE:

- Blinking Led sourced by DCO
- Blinking Led sourced by RTC
- Blinking Led sourced by RTC and USART Echo
- Toggle LED when BUT is pressed
- RF Connection_ press BUT
- RF Connection_toggle LED

ORDER CODE:

$MSP430\mathchar`-CCRF$ - assembled and tested board

How to order?

You can order to us directly or by any of our distributors. Check our web <u>www.olimex.com/dev</u> for more info.

Revision history:

Board's revision:	Rev. A, May 2011
Manual's revision:	Rev. A, June 2011
	- in BOARD FEATURES - added Supported radio frequencies bands, Maximal permissible distance between two MSP430-CCRF boards, RTC crystal and "Not typical power jack" changed to "PWR jack for 2 x 1.5V AA batteries "
	Rev. B, November 2011
	- BOARD FEATURES – added is table which describes maximal permissible distance between two MSP430-CCRF boards versus different RF settings.
	- <u>SCHEMATIC</u> – is updated to last version – with changed C22 and C23 values from 15pF to 10pF.
	- AVAILABLE DEMO SOFTWARE - "RF Connection_ press BUT to send a packet and toggle LED when packet is received" is changed to "RF Connection_ press BUT". "RF Connection_toggle LED" was added.

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