

# DATA SHEET

**BTA204W series D, E and F**  
Three quadrant triacs  
guaranteed commutation

Product specification

December 1998



# Three quadrant triacs guaranteed commutation

## BTA204W series D, E and F

### GENERAL DESCRIPTION

Passivated guaranteed commutation triacs in a plastic envelope suitable for surface mounting, intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series and "logic level" D series are intended for interfacing with low power drivers, including micro controllers.

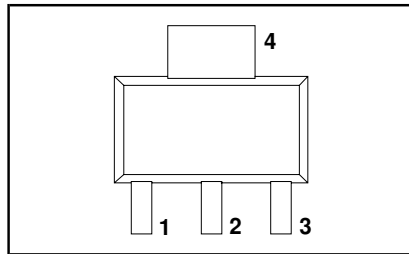
### QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
$V_{DRM}$	Repetitive peak off-state voltages	500D 500E 500F 500	600D 600E 600F 600	- 800E 800F 800	V
$I_{T(RMS)}$	RMS on-state current	1	1	1	A
$I_{TSM}$	Non-repetitive peak on-state current	10	10	10	A

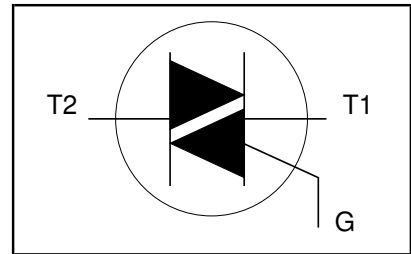
### PINNING - SOT223

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
tab	main terminal 2

### PIN CONFIGURATION



### SYMBOL



### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
				-500 500 <sup>1</sup>	-600 600 <sup>1</sup>	-800 800	
$V_{DRM}$	Repetitive peak off-state voltages		-				V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{sp} \leq 108\text{ }^\circ\text{C}$	-	1			A
$I_{TSM}$	Non-repetitive peak on-state current	full sine wave; $T_j = 25\text{ }^\circ\text{C}$ prior to surge	-	10			A
$I^2t$	$I^2t$ for fusing	$t = 20\text{ ms}$	-	11			A
$di_T/dt$	Repetitive rate of rise of on-state current after triggering	$t = 16.7\text{ ms}$ $t = 10\text{ ms}$ $I_{TM} = 1.5\text{ A};$ $I_G = 0.2\text{ A};$ $di_G/dt = 0.2\text{ A}/\mu\text{s}$	-	0.5			A <sup>2</sup> s
$I_{GM}$	Peak gate current		-	2			A
$V_{GM}$	Peak gate voltage		-	5			V
$P_{GM}$	Peak gate power		-	5			W
$P_{G(AV)}$	Average gate power	over any 20 ms period	-	0.5			W
$T_{stg}$	Storage temperature		-40	150			$^\circ\text{C}$
$T_j$	Operating junction temperature		-	125			$^\circ\text{C}$

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/ $\mu\text{s}$ .

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**THERMAL RESISTANCES**

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{thj-sp}$	Thermal resistance junction to solder point	full or half cycle	-	-	15	K/W
$R_{thj-a}$	Thermal resistance junction to ambient	pcb mounted; minimum footprint pcb mounted; pad area as in fig:2	-	156 70	-	K/W K/W

**STATIC CHARACTERISTICS**

$T_j = 25\text{ }^\circ\text{C}$  unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.			UNIT
<b>BTA204W-</b>								
$I_{GT}$	Gate trigger current <sup>2</sup>	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$ T2+ G+ T2+ G- T2- G-	-	-	...D	...E	...F	mA
$I_L$	Latching current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$ T2+ G+ T2+ G- T2- G-	-	-	5	10	25	mA
$I_H$	Holding current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$	-	-	5	10	25	mA
$V_T$	On-state voltage	$I_T = 2\text{ A}$	-	1.2	1.5			V
$V_{GT}$	Gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$ $V_D = 400\text{ V}; I_T = 0.1\text{ A};$ $T_j = 125\text{ }^\circ\text{C}$	-	0.7	1.5			V
$I_D$	Off-state leakage current	$V_D = V_{DRM(max)}$ $T_j = 125\text{ }^\circ\text{C}$	0.25	0.4	-			V
			-	0.1	0.5			mA

**DYNAMIC CHARACTERISTICS**

$T_j = 25\text{ }^\circ\text{C}$  unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.			TYP.	MAX.	UNIT
<b>BTA204W-</b>								
$dV_D/dt$	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}$ $T_j = 125\text{ }^\circ\text{C}$ ; exponential waveform; gate open circuit	...D	...E	...F	-	-	V/ $\mu\text{s}$
$di_{com}/dt$	Critical rate of change of commutating current	$V_{DM} = 400\text{ V}; T_j = 125\text{ }^\circ\text{C};$ $I_{T(RMS)} = 1\text{ A};$ $dV_{com}/dt = 20\text{ V}/\mu\text{s}$ ; gate open circuit	20	30	50	-	-	A/ms
$di_{com}/dt$	Critical rate of change of commutating current	$V_{DM} = 400\text{ V}; T_j = 125\text{ }^\circ\text{C};$ $I_{T(RMS)} = 1\text{ A};$ $dV_{com}/dt = 0.1\text{ V}/\mu\text{s}$ ; gate open circuit	1.0	2.0	2.5	-	-	A/ms
$t_{gt}$	Gate controlled turn-on time	$I_{TM} = 12\text{ A}; V_D = V_{DRM(max)}$ $I_G = 0.1\text{ A}; di_G/dt = 5\text{ A}/\mu\text{s}$	5.0	-	-	-	-	A/ms
			-	-	-	2	-	$\mu\text{s}$

<sup>2</sup> Device does not trigger in the T2-, G+ quadrant.

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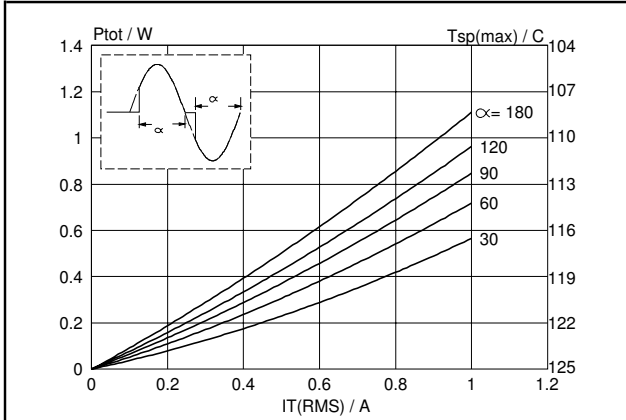


Fig.1. Maximum on-state dissipation,  $P_{tot}$ , versus rms on-state current,  $I_{T(RMS)}$ , where  $\alpha$  = conduction angle.

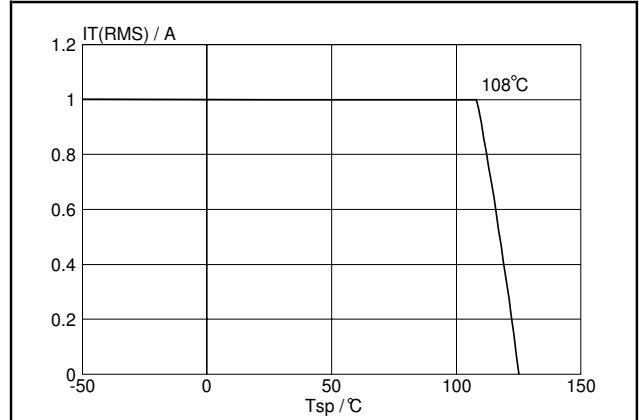


Fig.4. Maximum permissible rms current  $I_{T(RMS)}$ , versus solder point temperature  $T_{sp}$ .

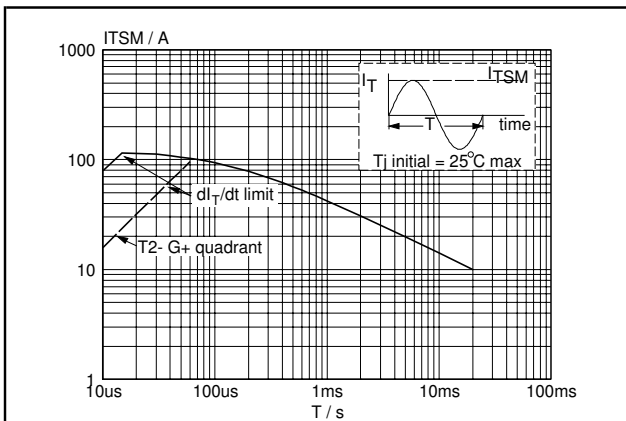


Fig.2. Maximum permissible non-repetitive peak on-state current  $I_{TSM}$ , versus pulse width  $t_p$ , for sinusoidal currents,  $t_p \leq 20ms$ .

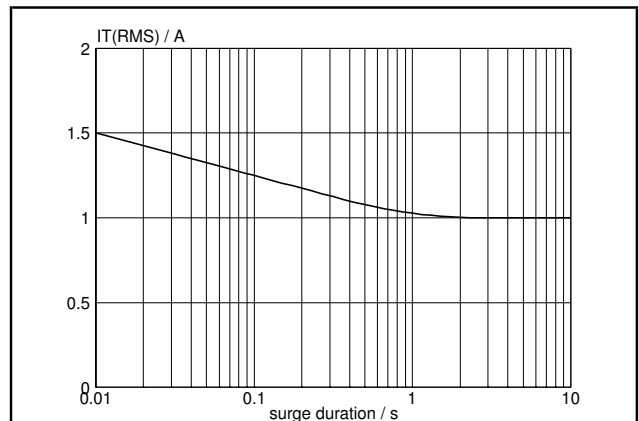


Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents,  $f = 50 Hz$ ;  $T_{sp} \leq 108^\circ C$ .

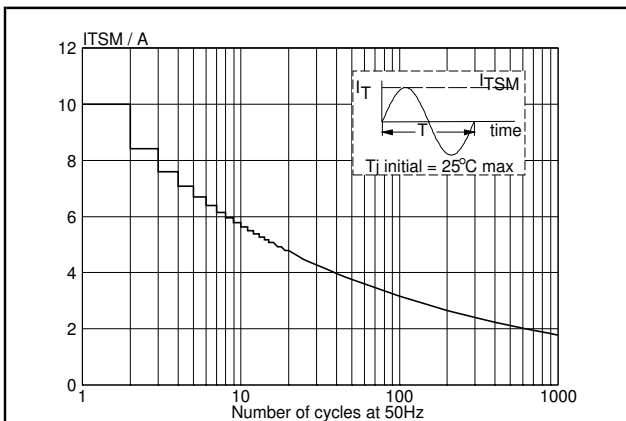


Fig.3. Maximum permissible non-repetitive peak on-state current  $I_{TSM}$ , versus number of cycles, for sinusoidal currents,  $f = 50 Hz$ .

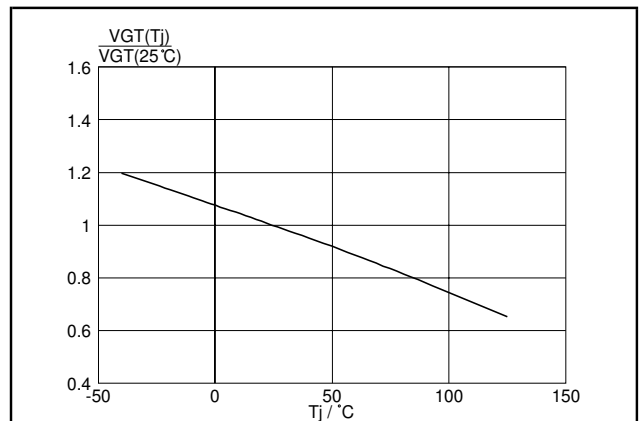
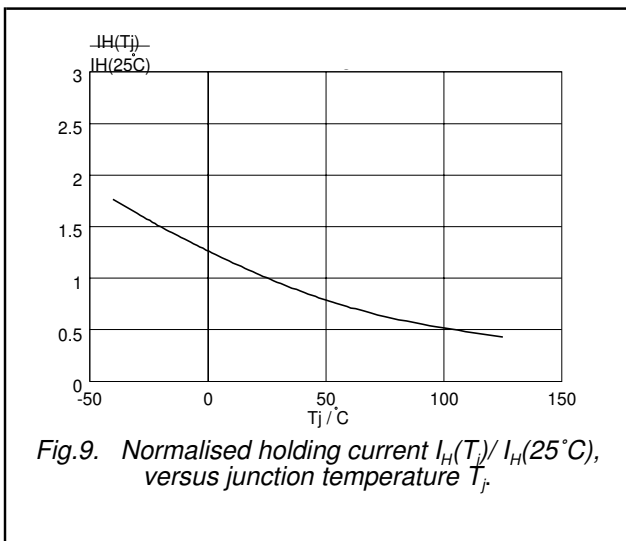
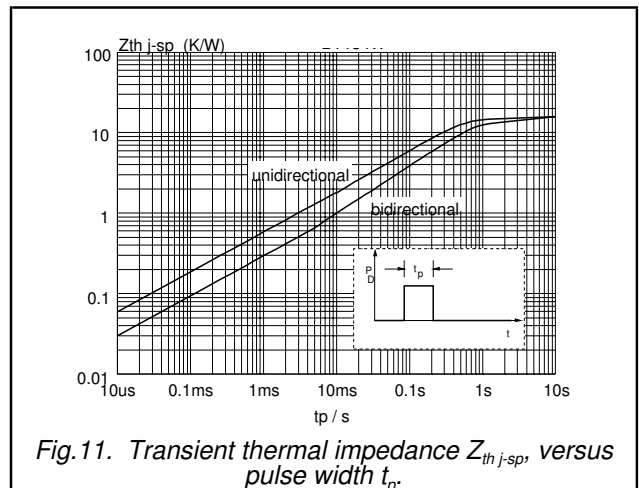
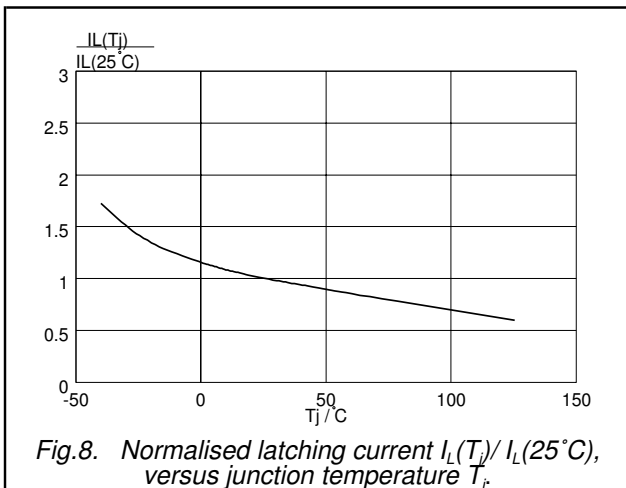
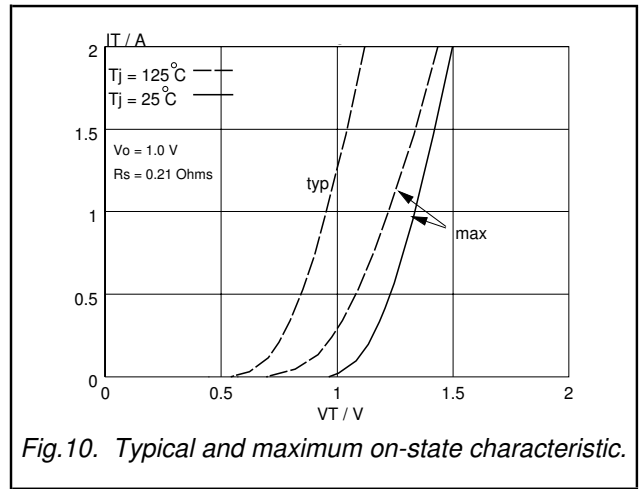
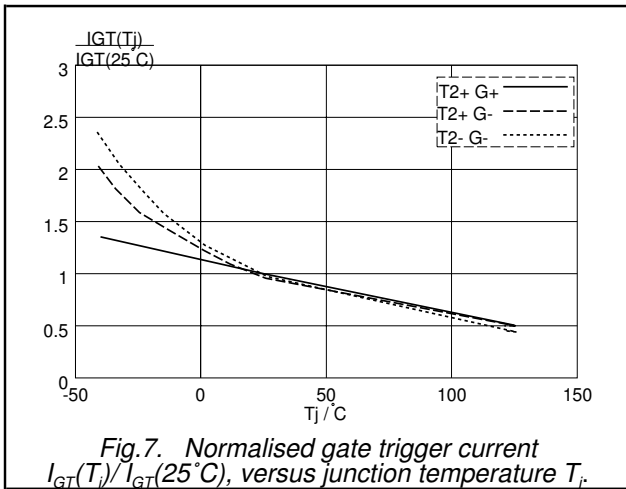


Fig.6. Normalised gate trigger voltage  $V_{GT(T_j)} / V_{GT(25^\circ C)}$ , versus junction temperature  $T_j$ .

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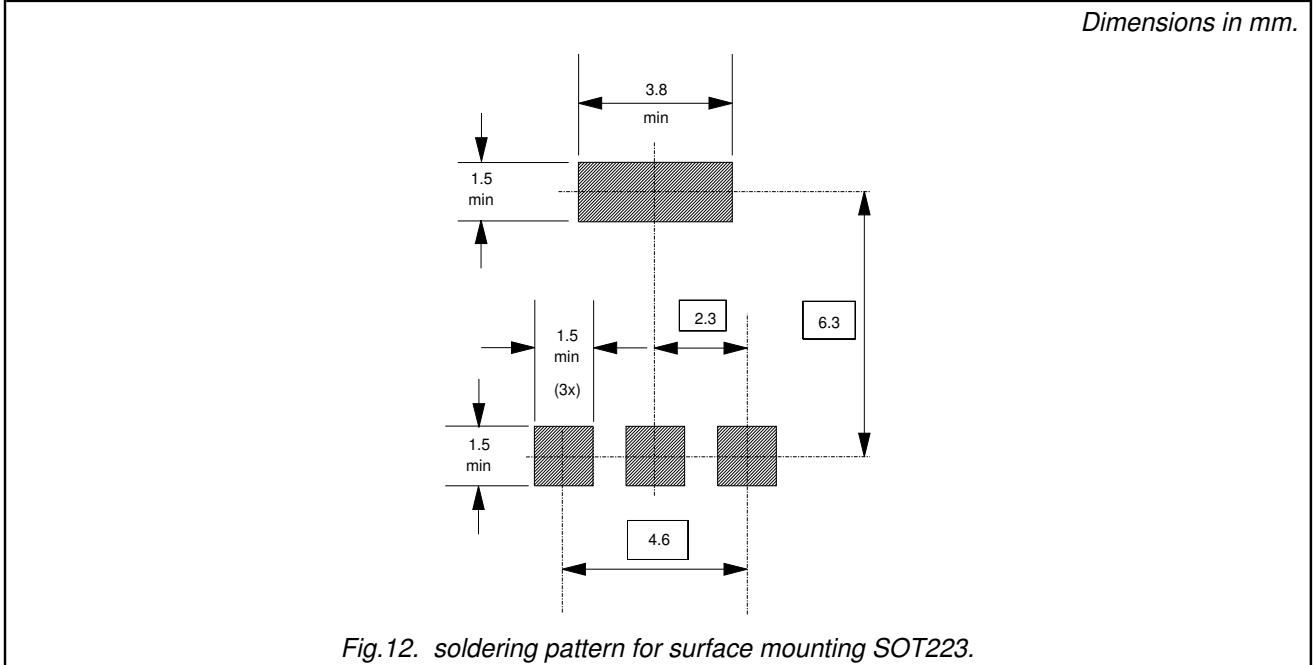
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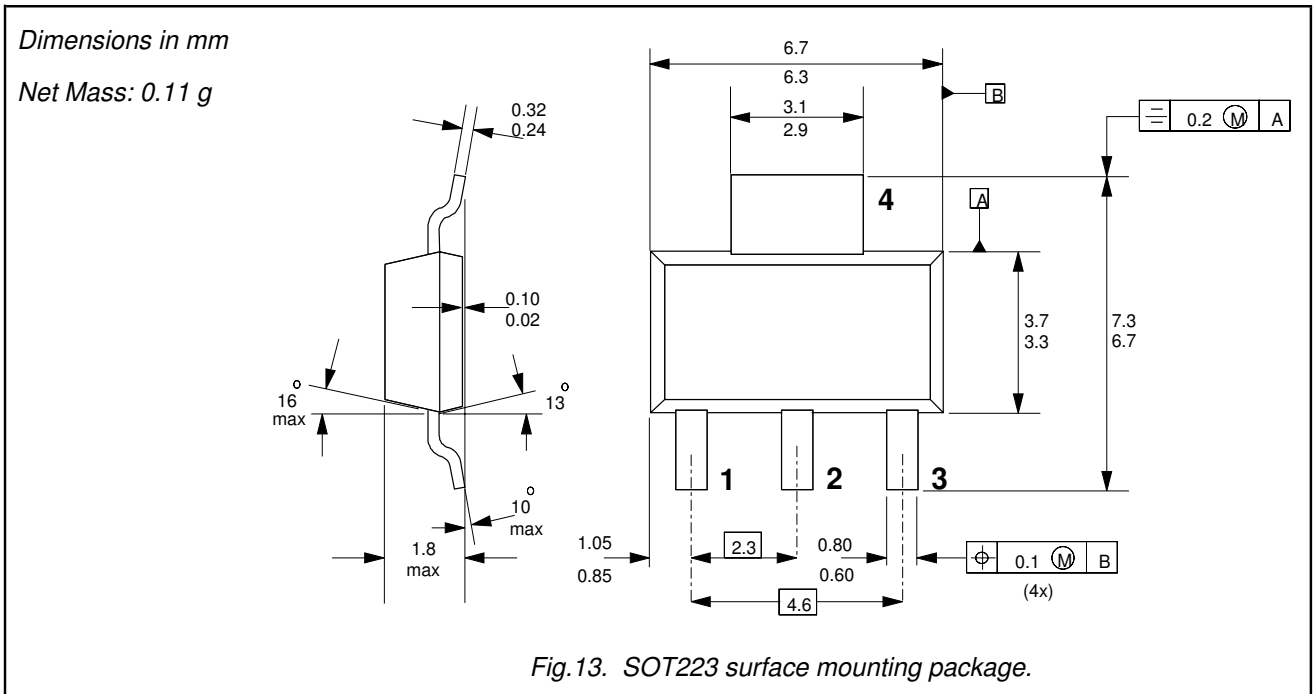
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**MOUNTING INSTRUCTIONS**



**MECHANICAL DATA**



**Notes**

1. For further information, refer to Philips publication SC18 " SMD Footprint Design and Soldering Guidelines".  
Order code: 9397 750 00505.
2. Epoxy meets UL94 V0 at 1/8".

## Legal information

### DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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