SN5445, SN7445 BCD-TO-DECIMAL DECODERS/DRIVERS

SDLS110

DECEMBER 1972-REVISED MARCH 1988

FOR USE AS LAMP, RELAY, OR MOS DRIVERS

featuring

- Full Decoding of Input Logic
- 80-mA Sink-Current Capability
- All Outputs Are Off for Invalid BCD Input Conditions

FUNCTION TABL	E
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NO. INPUTS			INPUTS OUTPUTS											
NO.	D	C	В	Α	0	1	2	3	_4	5	6	7	8	9
0	ΓL.	L	L	L	L	н	н	н	н	Н	н	н	H	Н
1	L	L	L	н	н	L	н	н	н	н	н	н	н	н
2	L	L	н	L	H	н	L	Н	н	Н	Н	Н	Н	н
3	L	L	Н	н	H.	Н	н	Ł	н	н	н	н	Н	н
4	L	н	L	Ł	н	Н	н	н	L	н	н	н	н	н
5	Ł	н	L	н	н	н	Н	н	н	Ł	н	Н	Н	н
6	L	н	н	L	н	н	н	н	н	н	L	H	н	н
7	L	н	н	н	н	н	н	н	н	н	н	L	Н	Н
8	Н	L	L	L	н	н	н	н	н	н	н	н	L.	н
9	н	L.	Ł	н	н	н	н	Н	H	H	н	Н	н	L
	H	L	Н	L	н	Н	H	Н	Н	Н	H	Н	Н	Н
	Н	L	н	н	н	н	н	н	н	н	н	н	н	н
INVALID	Н	Н	L	L	н	Н	н	н	н	н	н	Н	н	Н
$\frac{1}{2}$	н	н	L	н	н	н	н	Н	н	н	Н	н	Н	Н
=	н	н	н	L	н	н	н	Н	н	H	н	н	н	н
	н	н	н	Н	н	н	н	н	н	н	н	н	н	н

H = high level (off), L = low level (on)

description

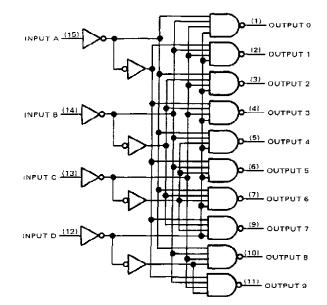
These monolithic BCD to decimal decoders/drivers consist of eight inverters and ten four-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of valid BCD input logic ensures that all outputs remain off for all invalid binary input conditions. These decoders feature TTL inputs and highperformance, n-p-n output transistors designed for use as indicator/relay drivers or as open-collector logiccircuit drivers. Each of the high-breakdown output transistors (30 volts) will sink up to 80 milliamperes of current. Each input is one normalized Series 54/74 load. Inputs and outputs are entirely compatible for use with TTL logic circuits, and the outputs are compatible for interfacing with most MOS integrated circuits. Power dissipation is typically 215 milliwatts.

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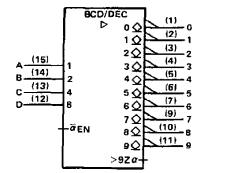


SN5445J OR W PACKAGE SN7445N PACKAGE (TOP VIEW)									
0			Vcc						
1		15	А						
2	₫3	14	В						
3	[]4	13	С						
4	<u></u> 5	12	D						
5	₫6		9						
6	7	105	8						
GND	[8	₀□	7						

logic diagram (positive logic)







Pin numbers shown are for J, N, and W packages.

SN5445, SN7445 BCD-TO-DECIMAL DECODERS/DRIVERS

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} (see Note 1)	7 V
Input voltage	.5 V
Maximum current into any output (off-state)	
Operating free-air temperature range: SN5445 Circuits	:5°C
SN7445 Circuits	0°C
Storage temperature range	0°C

NOTE 1: Voltage values are with respect to network ground terminal.

recommended operating conditions

		:	SN5445					
	N	ΛIN	NOM	MAX	MIN	NOM	MAX	
Supply voltage, V _{CC}		4.5	5	5.5	4.75	5	5.25	V
Off-state output voltage				30			30	V
Operating free-air temperature, T _A	-	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST CONDIT	TEST CONDITIONS [†]				
⊻ін	High-level input voltage			2			V
VIL	Low-level input voltage					0.8	V
Viк	Input clamp voltage	Vcc = MIN, II = -12 mA				1.5	V
V-	On-state output voltage	VCC = MIN, V _{tH} = 2 V,	lO(on) = 80 mA		0.5	0.9	v
VO(on)	Onstate output vonage	V _{1L} = 0.8 V	IO(on) = 20 mA			0.4	
ID(off)	Off-state output current	$V_{CC} = MIN, V_{IH} = 2V,$			250	μA	
ויינטוטי	Griatate output carrent	VIL = 0.8 V, VO(off) = 30 V			250	<u></u>	
1j	Input current at maximum input voltage	VCC = MAX, VI ≈ 5.5 V				1	mΑ
ηн	High-level input current	V _{CC} = MAX, V _I = 2.4 V				40	μA
IL.	Low-level input current	V _{CC} = MAX, V _I = 0.4 V				-1.6	mA
1		VCC = MAX, See Note 2	SN5445	43		62	-
lcc	Supply current	CC MAX, See Note 2	SN 7445		43	70	mA

[†]For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type. [‡]All typical values are at V_{CC} = 5 V, T_A = 25°C.

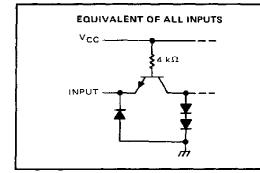
NOTE 2: I_{CC} is measured with all inputs grounded and outputs open.

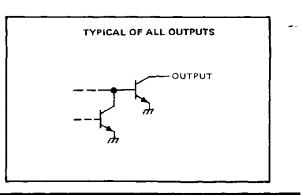
switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$

	PARAMETER	TEST CONDITIONS	MIN	түр	MAX	UNIT
TPLH	Propagation delay time, low-to-high-level output	C ₁ = 15 pF, R ₁ = 100 Ω, See Note 3			50	ns
TPHL	Propagation delay time, high-to-low-level output	CL - 15 pF, HL - 100 sr, See Note 5			50	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

schematics of inputs and outputs





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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
SN5445J	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	
SN7445N	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	
SN7445N	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	
SN7445N3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	
SN7445N3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	
SN7445NE4	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	
SN7445NE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	
SNJ5445J	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	
SNJ5445J	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	
SNJ5445W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	
SNJ5445W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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20-Aug-2011

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OTHER QUALIFIED VERSIONS OF SN5445, SN7445 :

Catalog: SN7445

Military: SN5445

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Military QML certified for Military and Defense Applications

J (R-GDIP-T**) 14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. This package can be hermetically sealed with a ceramic lid using glass frit.
 - D. Index point is provided on cap for terminal identification only.
 - E. Falls within MIL STD 1835 GDFP1-F16 and JEDEC MO-092AC



N (R-PDIP-T**)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



NOTES:

- A. All linear dimensions are in inches (millimeters).B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- \triangle The 20 pin end lead shoulder width is a vendor option, either half or full width.



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